

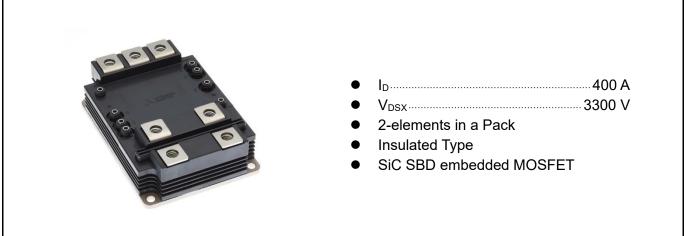
< HVMOSFET MODULE >

FMF400DC-66BEW

HIGH POWER SWITCHING USE INSULATED TYPE 2nd g

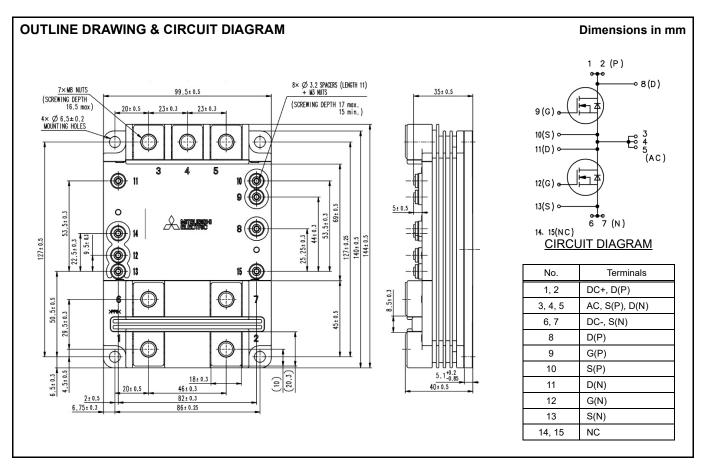
2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

FMF400DC-66BEW



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers



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INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

MAXIMUM RATINGS

Item	Symbol	Condition			Unit	
Drain-Source voltage, specified gate-source voltage	V _{DSX}	V _{GS} = -7 V	3300	V		
Gate-Source voltage	V _{GSS}	V _{DS} = 0 V	V _{DS} = 0 V T _i = -40~175 °C			
Drain current	I _D	V_{GS} = 17 V , T _c = 105 °C , AC terminal output current (Note 1)		400	А	
Drain current	I _{DP}	Non repetitive pulse	$T_j = T_{op}$	800	А	
Reverse drain current (FWD forward current)	I _S	V_{GS} = -7 V , T _c = 103 °C , AC terminal output current(Note 1)(Note 1)	ote 2)	400	А	
Reverse drain current (FWD forward current)	I _{SP}	Non repetitive pulse(Note 2)	$T_j = T_{op}$	800	А	
Total power dissipation	P _{tot}	T _c = 25 °C , MOSFET part(Note 3)	4160	W		
Isolation voltage	V _{isol}	Charge part to the baseplate RMS sinusoidal, 60Hz 1min	6000	V _{rms}		
Partial discharge charge	Q _{pd}	Charged part to the baseplate RMS sinusoidal, 60 Hz 1min V1 = 3500 V, V2 = 2600 V(acc. to IEC 61287-1)	10	рС		
Junction temperature	Tj	Maximum temperature range in off-state or on-state(non-switching	-40~175	°C		
Case temperature	T _c	Maximum case temperature range in on-state	-40~150	°C		
Storage temperature	T _{stg}	Maximum case temperature range in off-state	-50~175	°C		
Operating junction temperature	T _{jop}	Maximum junction temperature range for switching operation	-40~175	°C		
Short-circuit withstand pulse duration	t _{pSC}	V_{DD} = 2500 V , V_{GS} = +17 / -7 V , L_s = 40 nH , V_{GS} 50%- V_{GS} 50% T_j = T_{op}			μs	
Short circuit energy	E _{sc}	$V_{DD} = 2500 \text{ V}$, F(t)weibull=1% $T_j = T_{op}$		17.5	J	
Non-repetitive surge forward current	I _{FSM}	T _j = 175 °C		2.9	kA	
I2t value	l ² t		T _j = 175 °C	43	kA ² s	

ELECTRICAL CHARACTERISTICS

Item	Symbol	ol Condition -		Limits			Unit
	Cyrribol			Min.	Тур.	Max.	Onit
Gate-source leakage current	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = V_{GSS}$	T _j = 25 °C	-1.0	-	1.0	μA
		V _{DS} = V _{DSX} , V _{GS} = -7 V	T _i = 25 °C	-	0.002	-	mA
Drain-source cut-off current	I _{DSX}		T _j = 150 °C	-	0.025	-	mA
			T _j = 175 °C	-	0.040	1.5	mA
			T _i = 25 °C	1.60	2.10	2.60	V
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = 10 \text{ V}$, $I_{D} = 40 \text{mA}$	T _j = 150 °C	-	1.50	-	V
			T _i = 175 °C	0.90	1.45	1.90	V
			T _j = 25 °C	-	4.00	-	mΩ
Drain-source on resistance	r _{DS(on)}	$V_{DS} = V_{DS(on)}$, $V_{GS} = 17 V$	T _j = 150 °C	-	8.63	-	mΩ
			T _i = 175 °C	-	10.00	12.13	mΩ
Drain-source on-state voltage		I _D = 400 A , V _{GS} = 17 V , (Note 4)	T _j = 25 °C	-	1.60	-	V
	V _{DS(on)}		T _j = 150 °C	-	3.45	-	V
			T _j = 175 °C	-	4.00	4.85	V
		$V_{SD(on)}$ I _S = 400 A , V _{GS} = 17 V , (Note 4)	T _j = 25 °C	-	1.45	-	V
Source-drain voltage	V _{SD(on)}		T _i = 150 °C	-	3.25	-	V
			T _j = 175 °C	-	3.80	4.40	V
			T _i = 25 °C	-	2.00	-	V
Source-drain voltage	V _{SD}	I _S = 400 A , V _{GS} = 0 V , (Note 4)	T _i = 150 °C	-	3.85	-	V
			T _j = 175 °C	-	4.35	5.00	V
		I _S = 400 A , V _{GS} = -7 V , (Note 4)	T _i = 25 °C	-	2.00	-	V
Source-drain voltage	V _{SD(off)}		T _j = 150 °C	-	3.85	-	V
			T _j = 175 °C	-	4.35	5.00	V
Input capacitance	C _{iss}	V_{DS} = 10 V , V_{GS} = 0 V , f = 100kHz , 1/2 module	T _i = 25 °C	-	55	-	nF
Output capacitance	C _{oss}	V_{DS} = 10 V , V_{GS} = 0 V , f = 100kHz , 1/2 module	T _j = 25 °C	-	35	-	nF
Reverse transfer capacitance	C _{rss}	$V_{DS} = 10 \text{ V}$, $V_{GS} = 0 \text{ V}$, f = 100kHz , 1/2 module	T _j = 25 °C	-	1.4	-	nF
Gate charge	Q _G	V _{DD} = 1800 V , I _D = 400 A , V _{GS} = +17 / -7 V , 1/2 module	T _i = 25 °C	-	1.65	-	μC

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HIGH POWER SWITCHING USE

INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

ELECTRICAL CHARACTERISTICS

Item	Symbol	nbol Condition		Limits			Unit
nem	Symbol			Min.	Тур.	Max.	
Turn-on delay time	t _{d(on)}	V_{DD} = 1800 V , I_{D} = 400 A , V_{GS} = +17 / -7 V , L_{s} = 40 nH	T _i = 175 °C	-	-	0.41	μs
Rise time	t,	V_{DD} = 1800 V , I_{D} = 400 A , V_{GS} = +17 / -7 V , L_{s} = 40 nH	T _j = 175 °C	-	-	0.24	μs
		V _{nn} = 1800 V , I _n = 400 A , V _{ns} = +17 / -7 V , L _s = 40 nH	T _j = 25 °C	-	0.13	-	J
Turn-on (switching) energy per pulse 10% integral	E _{on(10%)}		T _i = 150 °C	-	0.11	-	J
		$R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 3.0 $\Omega,$ Inductive load	T _i = 175 °C	-	0.11	-	J
		V _{DD} = 1800 V , I _D = 400 A , V _{GS} = +17 / -7 V , L _s = 40 nH	T _j = 25 °C	-	0.14	-	J
Turn-on (switching) energy per pulse	Eon	$R_{G(on)} = 1.5 \Omega$, $R_{G(off)} = 3.0 \Omega$, Inductive load	T _j = 150 °C	-	0.12	-	J
		$T_{G(on)} = 1.5.22$, $T_{G(off)} = 5.0.22$, inductive load	T _i = 175 °C	-	0.12	-	J
Total capacitive charge	Q _c	V_{DD} = 1800 V , I_D = 400 A , V_{GS} = +17 / -7 V , L_s = 40 nH $R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 3.0 Ω , Inductive load	T _j = 25 °C	-	5.4	-	μC
			T _j = 150 °C	-	6.3	-	μC
			T _j = 175 °C	-	6.3	-	μC
Diode turn-off energy (per pulse)	E _{off_Diode(10%)}	V_{DD} = 1800 V , I_D = 400 A , V_{GS} = +17 / -7 V , L_s = 40 nH $R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 3.0 Ω , Inductive load	T _i = 25 °C	-	0.80	-	mJ
			T _j = 150 °C	-	-	-	mJ
			T _j = 175 °C	-	1.80	-	mJ
		V_{DD} = 1800 V , I_D = 400 A , V_{GS} = +17 / -7 V , L_s = 40 nH $R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 3.0 Ω , Inductive load	T _j = 25 °C	-	1.00	-	mJ
Diode switching off energy of diode	E_{off}_{Diode}		T _j = 150 °C	-	-	-	mJ
		TQ(on) = 1.5 22 ; TQ(off) = 5.6 22, Inductive load	T _j = 175 °C	-	2.00	-	mJ
Turn-off delay time	t _{d(off)}	V_{DD} = 1800 V , I_{D} = 400 A , V_{GS} = +17 / -7 V , L_{s} = 40 nH	T _i = 175 °C	-	-	0.94	μs
Fall time	t _f	V_{DD} = 1800 V , I_D = 400 A , V_{GS} = +17 / -7 V , L_s = 40 nH	T _i = 175 °C	-	-	0.40	μs
Turn-off (switching) energy per pulse 10% integral	E _{off(10%)}	V_{DD} = 1800 V , I_D = 400 A , V_{GS} = +17 / -7 V , L_s = 40 nH $R_{G(on)}$ = 1.5 Ω , $R_{G(off)}$ = 3.0 Ω , Inductive load	T _j = 25 °C	-	0.05	-	J
			T _j = 150 °C	-	0.06	-	J
			T _i = 175 °C	-	0.06	-	J
	E _{off}	V_{DD} = 1800 V , I_D = 400 A , V_{GS} = +17 / -7 V , L_s = 40 nH R _{G(on)} = 1.5 Ω , R _{G(off)} = 3.0 Ω , Inductive load	T _i = 25 °C	-	0.05	-	J
Turn-off (switching) energy per pulse			T _j = 150 °C	-	0.06	-	J
		$r_{G(on)} = 1.0.32$, $r_{G(ott)} = 0.0.32$, multiple load	T _i = 175 °C	-	0.06	-	J

THERMAL CHARACTERISTICS

Item Svm	Symbol	Condition		Limits		
Iterri Syribbi		Condition		Тур.	Max.	Unit
Thermal resistance junction to case	R _{th(j-c)}	Junction to Case, MOSFET + embeded SBD part, 1/2 module	-	-	36.0	K/kW
Contact thermal resistance case to heatsink	R _{th(c-s)}	Case to heat sink, λ_{grease} = 1W/m·K, $D_{(c-s)}$ = 70µm, 1/2 module	-	28.5	-	K/kW

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HIGH POWER SWITCHING USE

INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

MECHANICAL CHARACTERISTICS

Item	Symbol	ol Condition		Limits			
item	Symbol			Тур.	Max.	Unit	
Mounting torque	M _t	Vain terminal screw M8 This is the case when installing the product on the bus bar 7.0		-	22.0	N∙m	
Mounting torque	Mt	Mounting screw M6	3.0	-	6.0	N∙m	
Mounting torque	Mt	uxiliary terminals screw M3 0.4		-	0.8	N∙m	
mass	m			0.8	-	kg	
Comparative tracking index	CTI	- 6		-	-	-	
Clearance distance in air	d _a	Between main terminal 8		-	-	mm	
Creepage distance along surface	d _s	- 3		-	-	mm	
	L _{P DS}	Between DC+ and DC- (terminal1,2-6,7)	-	17	-	nH	
Internal inductance, D-S	L _{P DS}	Between DC+ and AC (terminal1,2-3,4,5)	-	45	-	nH	
	L _{P DS}	Between AC and DC- (terminal3,4,5-6,7)	-	45	-	nH	

Note 1. Control Case Temperature (T_c) so that the junction temperature (T_j) does not exceed the maximum rating.

Note 2. The symbols represent characteristics of the anti-parallel, source to drain free-wheel diode (FWDi).

Note 3. Junction temperature (T_j) should not exceed T_{jmax} rating.

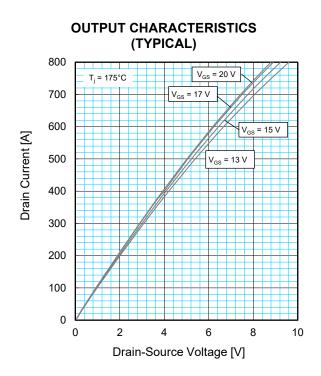
Note 4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

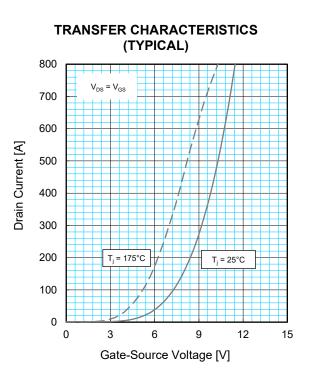
Products falling under the subject item No. 2 (41) 3 of Appended Table 1 of the Export Trade Control Order.

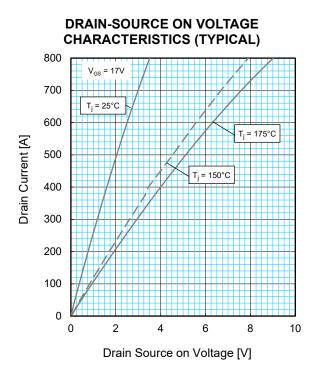
HIGH POWER SWITCHING USE

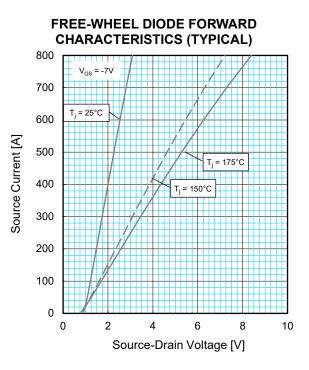
INSULATED TYPE 2nd gen. HVMOSFET (High Voltage Metal Oxide Semiconductor Field Effect Transistor) Modules

PERFPRMANCE CURVES





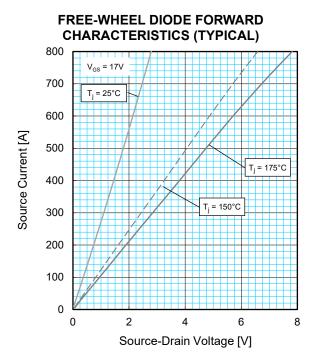


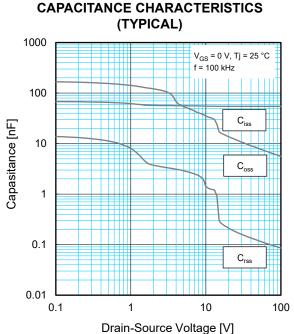


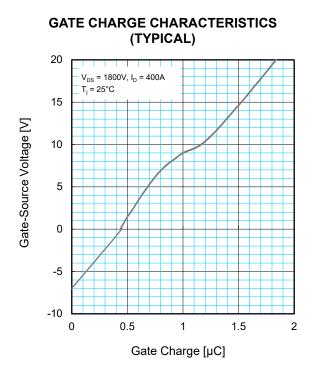
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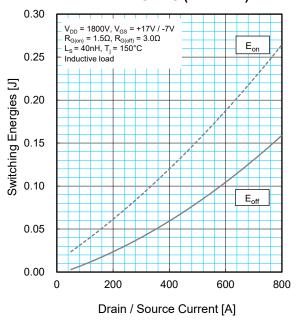
PERFORMANCE CURVES





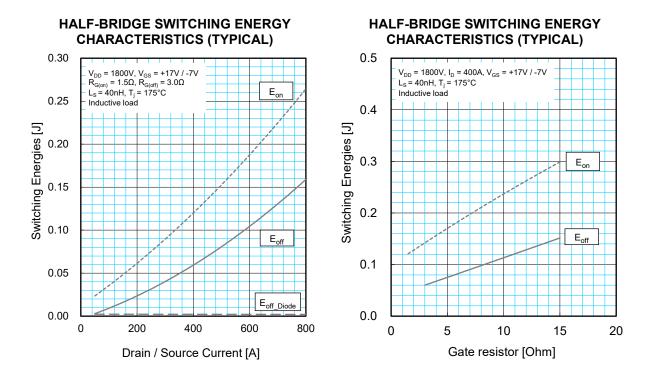


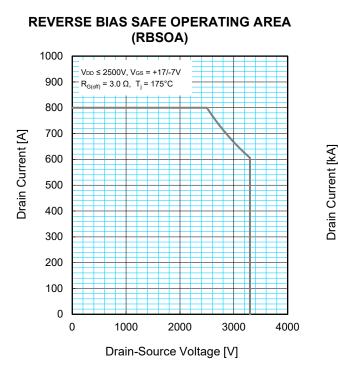
HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



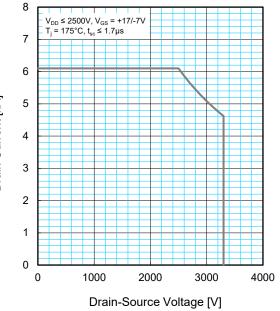
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PERFORMANCE CURVES



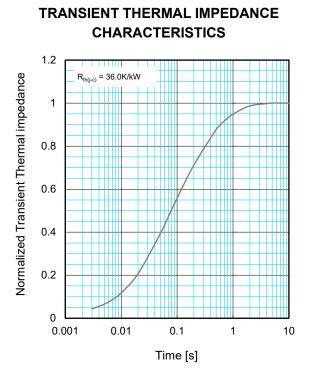


SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



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PERFORMANCE CURVES



$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - exp^{\left(-\frac{t}{\tau_{i}}\right)} \right\}$

	1	2	3	4
R_i/R_{th} :	0.0078	0.1975	0.3553	0.4393
τι [sec.] :	0.0001	0.7324	0.0381	0.1698

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