

< High Voltage Insulated Gate Bipolar Transistor : HVIGBT >

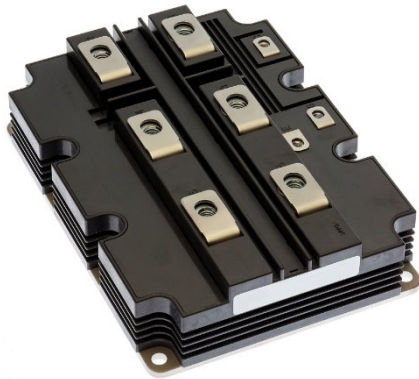
# CM900E2G-90X

HIGH POWER SWITCHING USE

INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## CM900E2G-90X



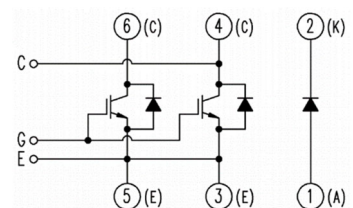
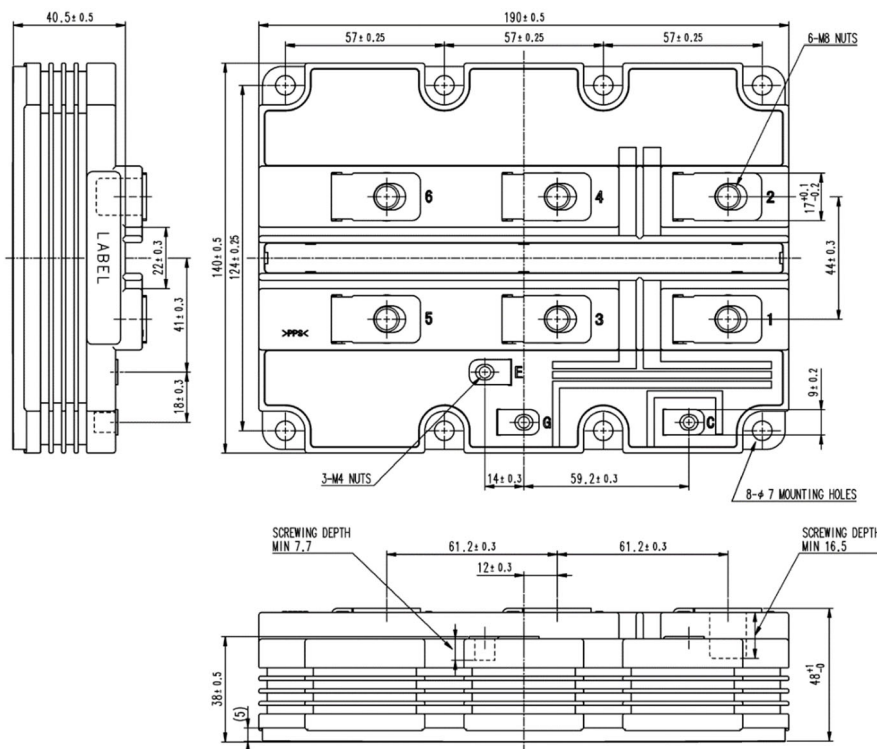
- $I_C$ .....900 A
- $V_{CES}$ .....4500 V
- 1-element in pack
- High Insulated type
- CSTBT™(III) / RFC Diode
- AISiC baseplate

## APPLICATION

Brake chopper

## OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



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CIRCUIT DIAGRAM

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**MAXIMUM RATINGS**

Symbol	Item	Conditions	Ratings	Unit
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0 V, T <sub>j</sub> = -40...+150 °C	4500	V
		V <sub>GE</sub> = 0 V, T <sub>j</sub> = -50 °C	4400	
V <sub>RRM</sub>	Repetitive peak reverse voltage (Note 3)	V <sub>GE</sub> = 0 V, T <sub>j</sub> = -40...+150 °C	4500	V
		V <sub>GE</sub> = 0 V, T <sub>j</sub> = -50 °C	4400	
V <sub>RSM</sub>	Non-repetitive peak reverse voltage (Note 3)	V <sub>GE</sub> = 0 V, T <sub>j</sub> = -40...+150 °C	4500	V
		V <sub>GE</sub> = 0 V, T <sub>j</sub> = -50 °C	4400	
V <sub>GES</sub>	Gate-emitter voltage	V <sub>CE</sub> = 0 V, T <sub>j</sub> = 25 °C	± 20	V
I <sub>C</sub>	Collector current	DC, T <sub>c</sub> = 105 °C	900	A
I <sub>CRM</sub>		Pulse (Note 1)	1800	
I <sub>E</sub>	Emitter current (Note 2)	DC, T <sub>c</sub> = 90 °C	900	A
I <sub>ERM</sub>		Pulse (Note 1)	1800	
I <sub>F</sub>	Forward current (Note 3)	DC, T <sub>c</sub> = 90 °C	900	A
I <sub>FRM</sub>		Pulse (Note 1)	1800	
I <sub>FSM</sub>	Surge forward current (Note 3)	T <sub>j, start</sub> = 150 °C, t <sub>p</sub> = 10ms, V <sub>R</sub> = 0 V	8.1	kA
I <sup>2</sup> t	Surge current load integral (Note 3)	F(t) = 1 %, Half-sine wave	328	kA <sup>2</sup> s
P <sub>tot</sub>	Maximum power dissipation (Note 4)	T <sub>c</sub> = 25 °C, IGBT part	9800	W
V <sub>iso</sub>	Isolation voltage	RMS, sinusoidal, f = 60 Hz, t = 1 min.	10200	V
Q <sub>PD</sub>	Partial discharge	Charged part to the baseplate V1 = 6900 Vrms, V2 = 5100 Vrms AC 60 Hz, T <sub>c</sub> = 25 °C (acc. to IEC 61287)	10	pC
T <sub>j</sub>	Junction temperature	—	-50 ~ +150	°C
T <sub>jop</sub>	Operating junction temperature	—	-50 ~ +150	°C
T <sub>stg</sub>	Storage temperature	—	-55 ~ +150	°C
t <sub>psc</sub>	Short circuit pulse width	V <sub>CC</sub> = 3400 V, V <sub>CE</sub> ≤ V <sub>CES</sub> , V <sub>GE</sub> = ±15 V, T <sub>j</sub> = 150 °C R <sub>G(on)</sub> = 3.6 Ω, R <sub>G(off)</sub> = 45 Ω, L <sub>S</sub> ≤ 225 nH	10	μs

**ELECTRICAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit	
			Min	Typ	Max		
I <sub>CES</sub>	Collector cutoff current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V	T <sub>j</sub> = 25 °C	—	—	4.0	mA
			T <sub>j</sub> = 125 °C	—	4.0	—	
			T <sub>j</sub> = 150 °C	—	—	80	
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 90 mA, T <sub>j</sub> = 25 °C	6.5	7.0	7.5	V	
I <sub>GES</sub>	Gate leakage current	V <sub>CE</sub> = 0 V, V <sub>GE</sub> = V <sub>GES</sub> , T <sub>j</sub> = 25 °C	-0.5	—	0.5	μA	
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 10 V, V <sub>GE</sub> = 0 V, f = 100 kHz T <sub>j</sub> = 25 °C	—	115	—	nF	
C <sub>oes</sub>	Output capacitance		—	7.5	—	nF	
C <sub>res</sub>	Reverse transfer capacitance		—	1.0	—	nF	
Q <sub>G</sub>	Total gate charge	V <sub>CC</sub> = 2800 V, I <sub>C</sub> = 900 A, V <sub>GE</sub> = ±15 V, T <sub>j</sub> = 25 °C	—	8.4	—	μC	
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> = 900 A (Note 5) V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25 °C	—	2.25	—	V
			T <sub>j</sub> = 125 °C	—	2.90	—	
			T <sub>j</sub> = 150 °C	—	3.00	3.50	
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> = 2800 V I <sub>C</sub> = 900 A V <sub>GE</sub> = ±15 V R <sub>G(on)</sub> = 3.6 Ω L <sub>S</sub> = 225 nH Inductive load	T <sub>j</sub> = 150 °C	—	—	0.90	μs
t <sub>r</sub>	Rise time		T <sub>j</sub> = 150 °C	—	—	0.50	μs
E <sub>on(10%)</sub>	Turn-on switching energy (Note 6) per pulse		T <sub>j</sub> = 25 °C	—	4.10	—	J
		T <sub>j</sub> = 125 °C	—	4.40	—		
		T <sub>j</sub> = 150 °C	—	4.45	—		
E <sub>on</sub>	Turn-on switching energy per pulse	Inductive load	T <sub>j</sub> = 25 °C	—	4.15	—	J
			T <sub>j</sub> = 125 °C	—	4.60	—	
			T <sub>j</sub> = 150 °C	—	4.65	—	

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Symbol	Item	Conditions	Limits			Unit	
			Min	Typ	Max		
$t_{d(off)}$	Turn-off delay time	$V_{CC} = 2800\text{ V}$ $I_C = 900\text{ A}$ $V_{GE} = \pm 15\text{ V}$ $R_{G(off)} = 45\ \Omega$ $L_S = 225\text{ nH}$ Inductive load	$T_j = 25\text{ }^\circ\text{C}$	—	—	—	$\mu\text{s}$
			$T_j = 125\text{ }^\circ\text{C}$	—	7.00	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	7.20	10.0	
$t_f$	Fall time		$T_j = 25\text{ }^\circ\text{C}$	—	—	—	$\mu\text{s}$
			$T_j = 125\text{ }^\circ\text{C}$	—	0.50	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	0.50	1.20	
$E_{off(10\%)}$	Turn-off switching energy (Note 6) per pulse		$T_j = 25\text{ }^\circ\text{C}$	—	2.60	—	J
			$T_j = 125\text{ }^\circ\text{C}$	—	3.55	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	3.75	—	
$E_{off}$	Turn-off switching energy per pulse	$T_j = 25\text{ }^\circ\text{C}$	—	2.90	—	J	
		$T_j = 125\text{ }^\circ\text{C}$	—	3.95	—		
		$T_j = 150\text{ }^\circ\text{C}$	—	4.15	—		
$V_{EC}$	Emitter-collector voltage (Note 2)	$I_E = 900\text{ A}$ (Note 5) $V_{GE} = 0\text{ V}$	$T_j = 25\text{ }^\circ\text{C}$	—	2.35	—	V
			$T_j = 125\text{ }^\circ\text{C}$	—	2.90	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	3.00	3.50	
$t_{rr}$	Reverse recovery time (Note 2)		$T_j = 25\text{ }^\circ\text{C}$	—	—	—	$\mu\text{s}$
			$T_j = 125\text{ }^\circ\text{C}$	—	1.60	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	1.85	—	
$I_{rr}$	Reverse recovery current (Note 2)		$T_j = 25\text{ }^\circ\text{C}$	—	—	—	A
			$T_j = 125\text{ }^\circ\text{C}$	—	1300	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	1300	—	
$Q_{rr(10\%)}$	Reverse recovery charge (Note 2.7)	$T_j = 25\text{ }^\circ\text{C}$	—	—	—	$\mu\text{C}$	
		$T_j = 125\text{ }^\circ\text{C}$	—	1830	—		
		$T_j = 150\text{ }^\circ\text{C}$	—	1870	—		
$Q_{rr}$	Reverse recovery charge (Note 2)	$T_j = 25\text{ }^\circ\text{C}$	—	—	—	$\mu\text{C}$	
		$T_j = 125\text{ }^\circ\text{C}$	—	1910	—		
		$T_j = 150\text{ }^\circ\text{C}$	—	1930	—		
$E_{rec(10\%)}$	Reverse recovery energy (Note 2.6) per pulse	$T_j = 25\text{ }^\circ\text{C}$	—	2.30	—	J	
		$T_j = 125\text{ }^\circ\text{C}$	—	3.00	—		
		$T_j = 150\text{ }^\circ\text{C}$	—	3.10	—		
$E_{rec}$	Reverse recovery energy (Note 2) per pulse	$T_j = 25\text{ }^\circ\text{C}$	—	2.35	—	J	
		$T_j = 125\text{ }^\circ\text{C}$	—	3.20	—		
		$T_j = 150\text{ }^\circ\text{C}$	—	3.25	—		

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5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## ELECTRICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit	
			Min	Typ	Max		
$I_{RRM}$	Repetitive reverse current (Note 3)	$V_{AK} = V_{RRM}$	$T_j = 25\text{ }^\circ\text{C}$	—	—	1.6	mA
			$T_j = 125\text{ }^\circ\text{C}$	—	1.6	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	—	32	
$V_F$	Forward voltage (Note 3)	$I_F = 900\text{ A}$ (Note 5)	$T_j = 25\text{ }^\circ\text{C}$	—	2.35	—	V
			$T_j = 125\text{ }^\circ\text{C}$	—	2.90	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	3.00	3.50	
$t_{rr}$	Reverse recovery time (Note 3)		$T_j = 25\text{ }^\circ\text{C}$	—	—	—	$\mu\text{s}$
			$T_j = 125\text{ }^\circ\text{C}$	—	1.60	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	1.85	—	
$I_{rr}$	Reverse recovery current (Note 3)		$T_j = 25\text{ }^\circ\text{C}$	—	—	—	A
			$T_j = 125\text{ }^\circ\text{C}$	—	1300	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	1300	—	
$Q_{rr(10\%)}$	Reverse recovery charge (Note 3.7)	$V_{CC} = 2800\text{ V}$ $I_F = 900\text{ A}$ $-di_F/dt \cong$ $3000\text{ A}/\mu\text{s}$ @ $T_j = 25\text{ }^\circ\text{C}$ $2800\text{ A}/\mu\text{s}$ @ $T_j = 125\text{ }^\circ\text{C}$ $2700\text{ A}/\mu\text{s}$ @ $T_j = 150\text{ }^\circ\text{C}$ $L_S = 225\text{ nH}$ Inductive load	$T_j = 25\text{ }^\circ\text{C}$	—	—	—	$\mu\text{C}$
			$T_j = 125\text{ }^\circ\text{C}$	—	1830	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	1870	—	
$Q_{rr}$	Reverse recovery charge (Note 3)		$T_j = 25\text{ }^\circ\text{C}$	—	—	—	$\mu\text{C}$
			$T_j = 125\text{ }^\circ\text{C}$	—	1910	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	1930	—	
$E_{rec(10\%)}$	Reverse recovery energy (Note 3.6) per pulse		$T_j = 25\text{ }^\circ\text{C}$	—	2.30	—	J
			$T_j = 125\text{ }^\circ\text{C}$	—	3.00	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	3.10	—	
$E_{rec}$	Reverse recovery energy (Note 3) per pulse		$T_j = 25\text{ }^\circ\text{C}$	—	2.35	—	J
			$T_j = 125\text{ }^\circ\text{C}$	—	3.20	—	
			$T_j = 150\text{ }^\circ\text{C}$	—	3.25	—	

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## THERMAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
$R_{th(j-c)Q}$	Thermal resistance	Junction to Case, IGBT part	—	—	12.8	K/kW
$R_{th(j-c)D}$	Thermal resistance <sup>(Note 2)</sup>	Junction to Case, FWDi part	—	—	19.5	K/kW
$R_{th(j-c)D}$	Thermal resistance <sup>(Note 3)</sup>	Junction to Case, Clamp-Di part	—	—	19.5	K/kW
$R_{th(c-s)}$	Contact thermal resistance <sup>(Note 2)</sup>	Case to heat sink, Switching part $\lambda_{grease} = 1 \text{ W/m}\cdot\text{K}$ , $D_{(c-s)} = 80 \mu\text{m}$	—	7.5	—	K/kW
$R_{th(c-s)}$	Contact thermal resistance <sup>(Note 3)</sup>	Case to heat sink, Clamp-Di part $\lambda_{grease} = 1 \text{ W/m}\cdot\text{K}$ , $D_{(c-s)} = 80 \mu\text{m}$	—	15.0	—	K/kW

## MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
$M_t$	Mounting torque	M8 : Main terminals screw	7.0	—	19.0	N·m
$M_s$		M6 : Mounting screw	3.0	—	6.0	N·m
$M_t$		M4 : Auxiliary terminals screw	1.0	—	3.0	N·m
$M$	Mass		—	1.5	—	kg
CTI	Comparative tracking index		600	—	—	—
$d_a$	Clearance		26.0	—	—	mm
$d_s$	Creepage distance		56.0	—	—	mm
$L_{P(C-E)}$	Internal inductance	Collector to Emitter	—	20.5	—	nH
$L_{P(A-K)}$		Anode to Cathode	—	41.0	—	nH
$R_{CC+EE}$	Internal lead resistance	$T_C = 25 \text{ }^\circ\text{C}$ , Collector to Emitter	—	0.18	—	m $\Omega$
$R_{AA+KK}$		$T_C = 25 \text{ }^\circ\text{C}$ , Anode to Cathode	—	0.36	—	m $\Omega$

Note 1. Pulse width and repetition rate should be such that junction temperature ( $T_j$ ) does not exceed  $T_{jopmax}$  rating.

Note 2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD<sub>i</sub>).

Note 3. The symbols represent characteristics of the clamp diode (Clamp-Di).

Note 4. Junction temperature ( $T_j$ ) should not exceed  $T_{jmax}$  rating (150°C).

Note 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

Note 6. The integration range of switching energies is from 10% $V_{CE}$  to 10% $I_C$ (10% $I_E$ ).

Note 7. The integration range of reverse recovery charge is from  $I_E = 0\text{A}$  to 10% $I_E$ .

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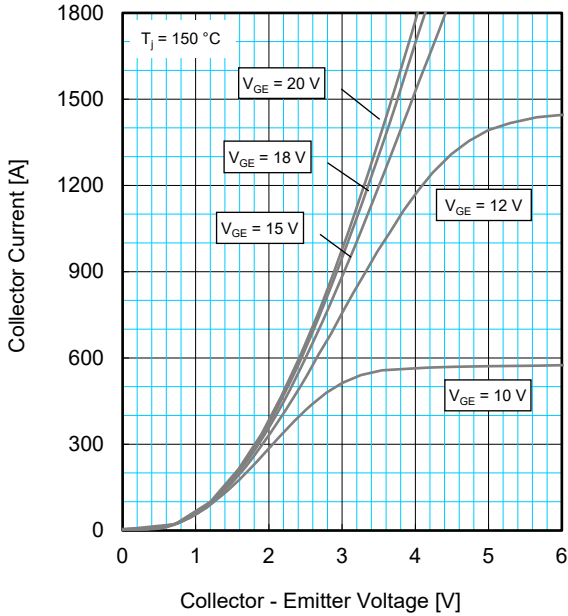
HIGH POWER SWITCHING USE

INSULATED TYPE

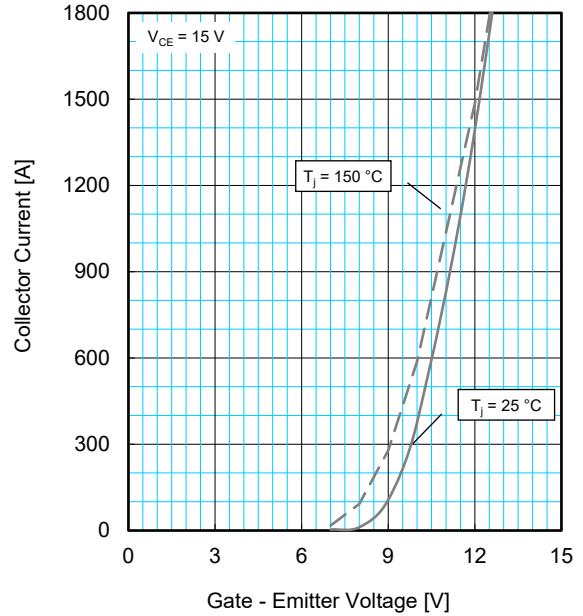
5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## PERFORMANCE CURVES

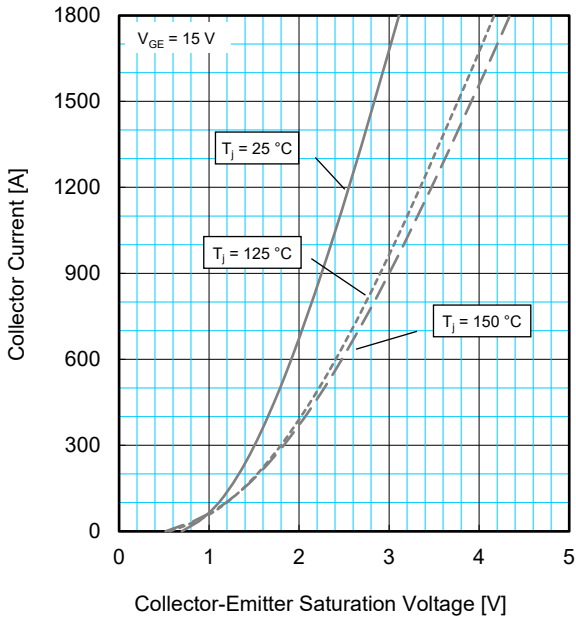
**OUTPUT CHARACTERISTICS (TYPICAL)**



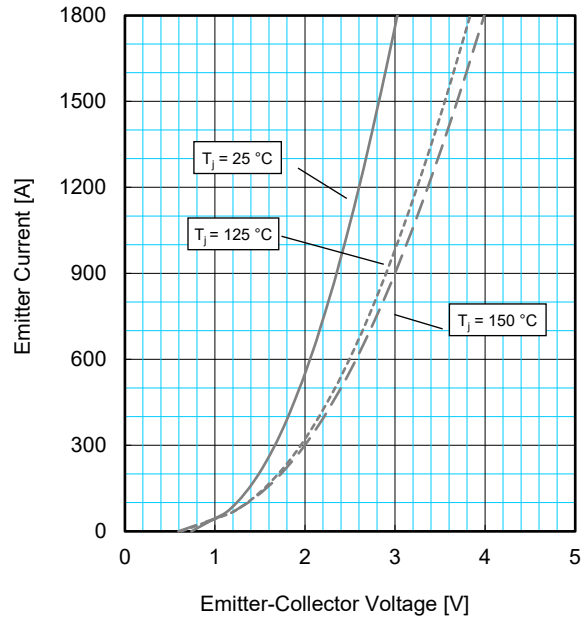
**TRANSFER CHARACTERISTICS (TYPICAL)**



**COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)**



**FREE-WHEEL DIODE / CLAMP DIODE FORWARD CHARACTERISTICS (TYPICAL)**



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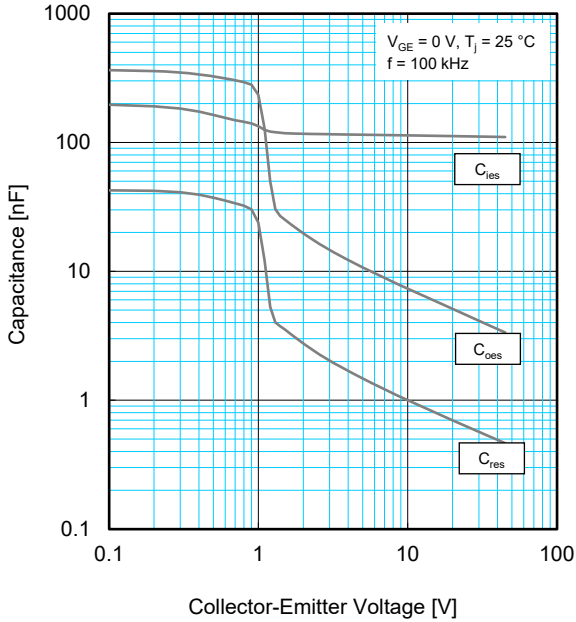
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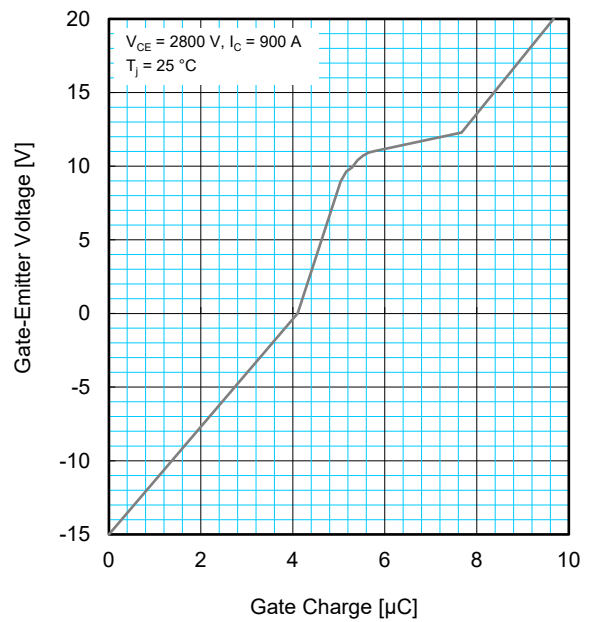
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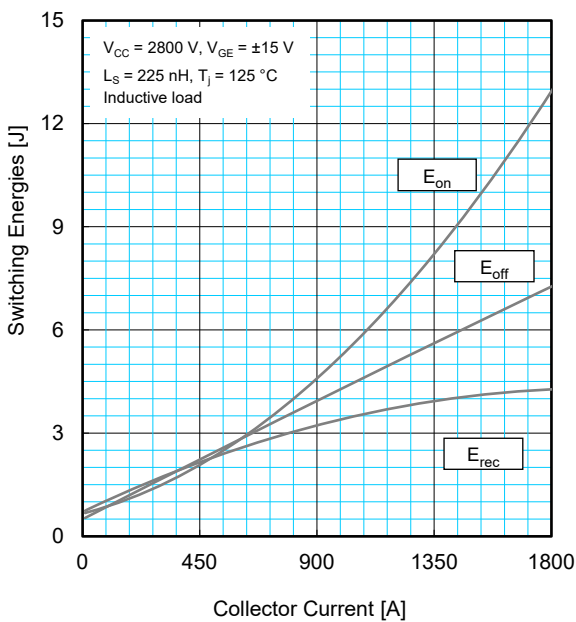
**CAPACITANCE CHARACTERISTICS (TYPICAL)**



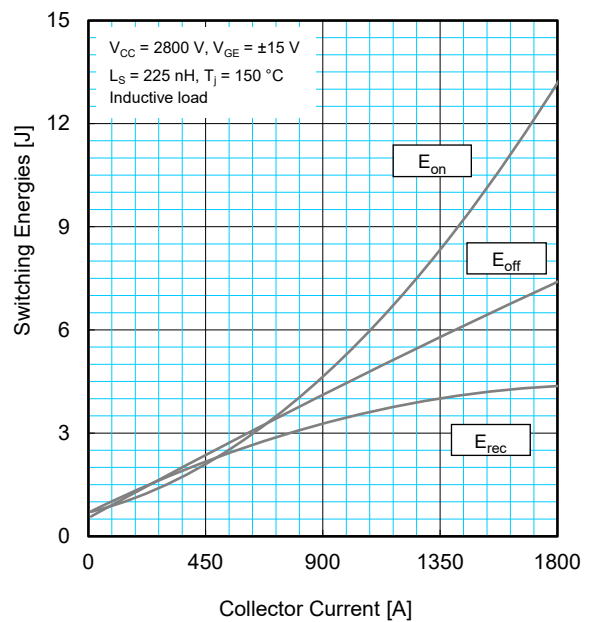
**GATE CHARGE CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



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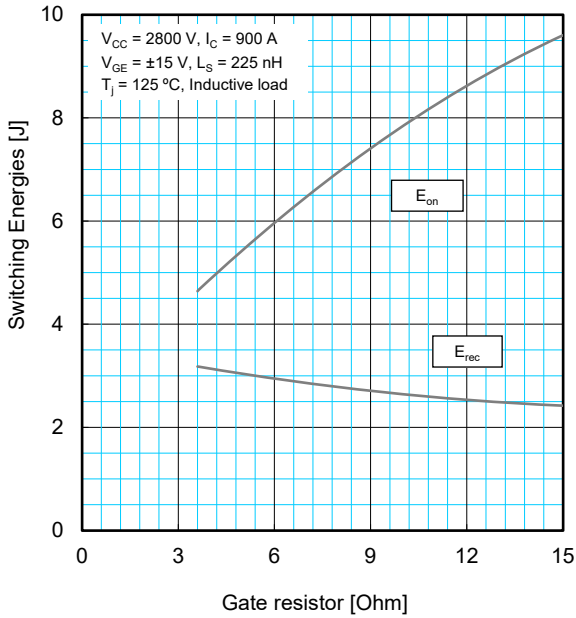
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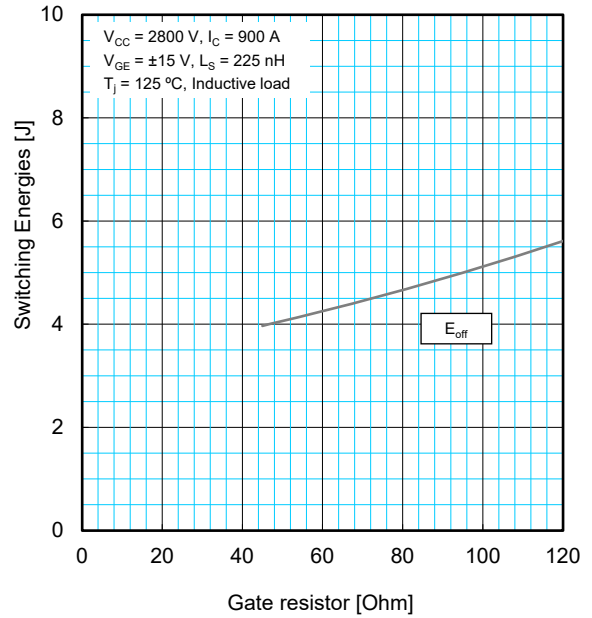
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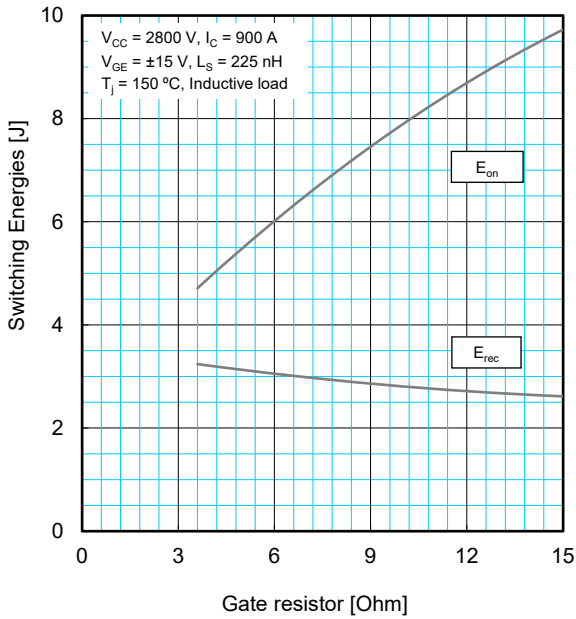
**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



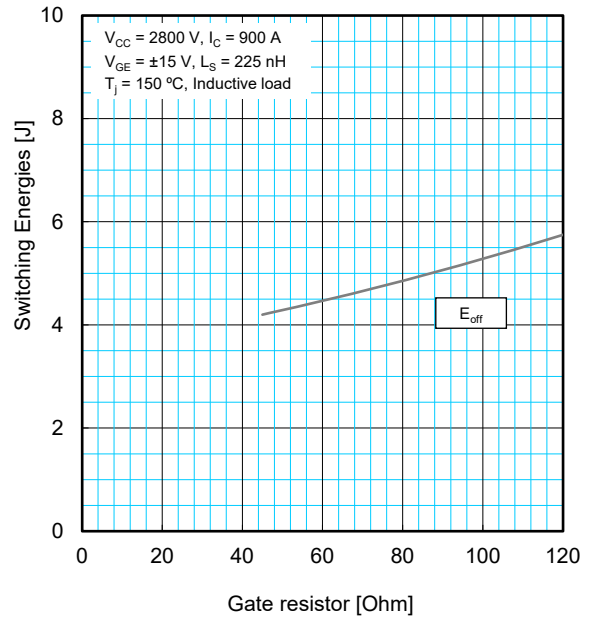
**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



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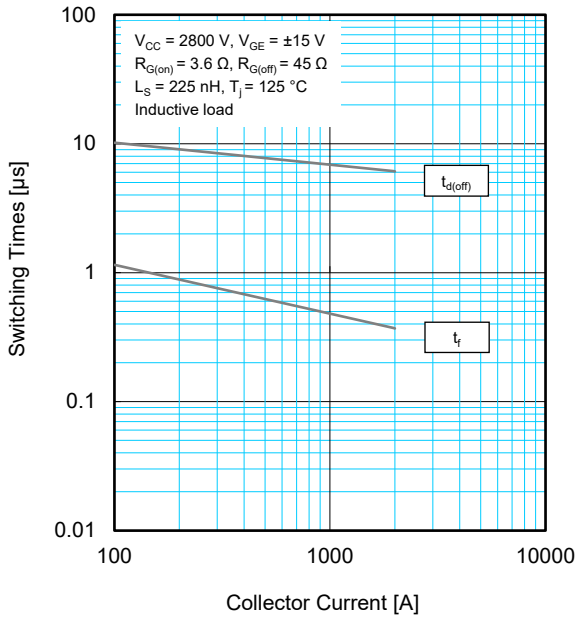
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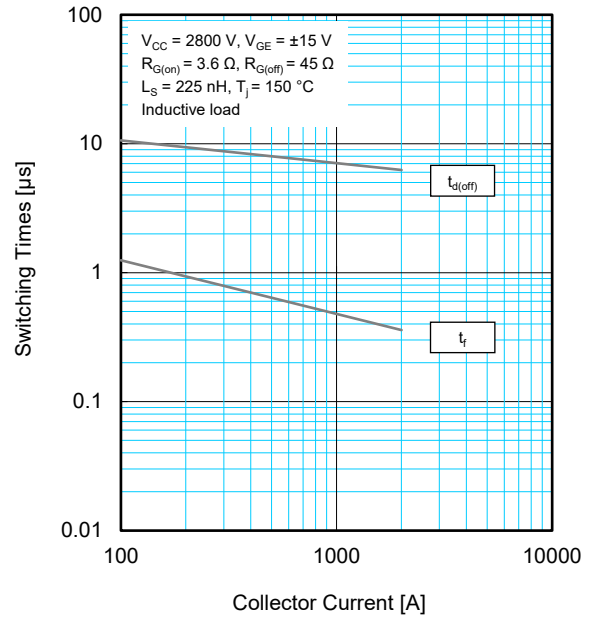
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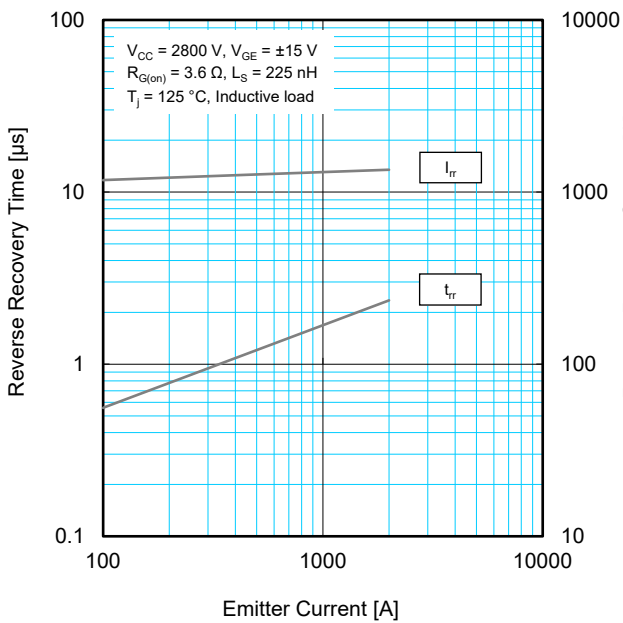
**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)**



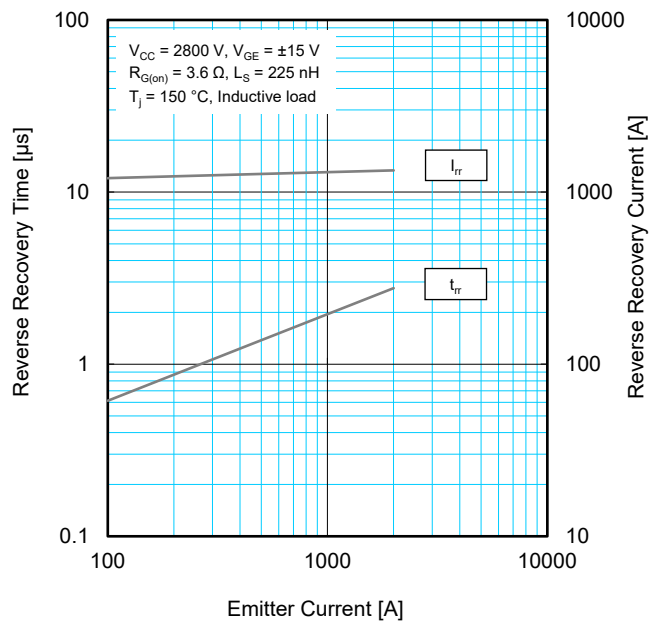
**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)**



**FREE-WHEEL DIODE / CLAMP DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)**



**FREE-WHEEL DIODE / CLAMP DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)**



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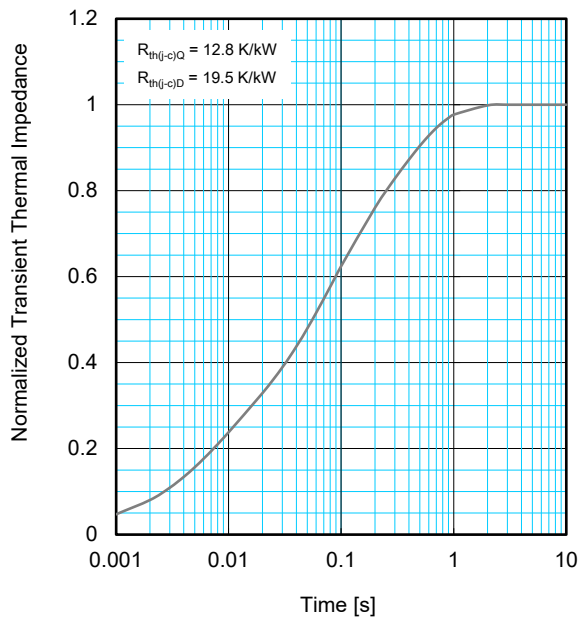
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## PERFORMANCE CURVES

**TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS**



$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i \left\{ 1 - \exp\left(-\frac{t}{\tau_i}\right) \right\}$$

	1	2	3	4
$R_i / R_{th}$ :	0.0096	0.1893	0.4044	0.3967
$\tau_i$ [sec.] :	0.0001	0.0058	0.0602	0.3512

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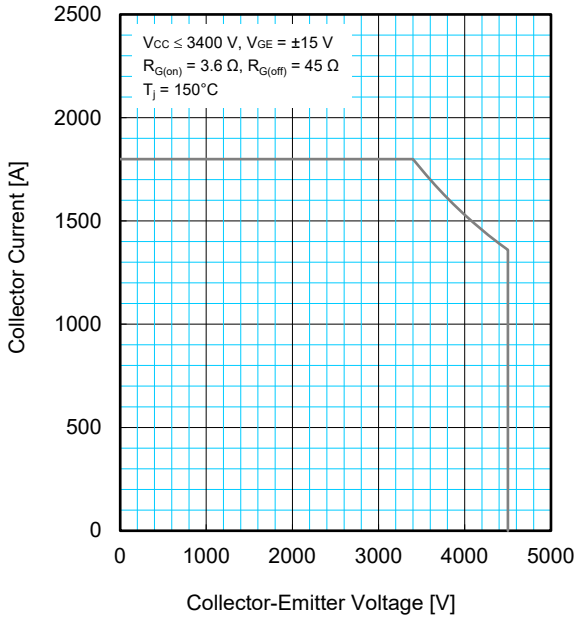
HIGH POWER SWITCHING USE

INSULATED TYPE

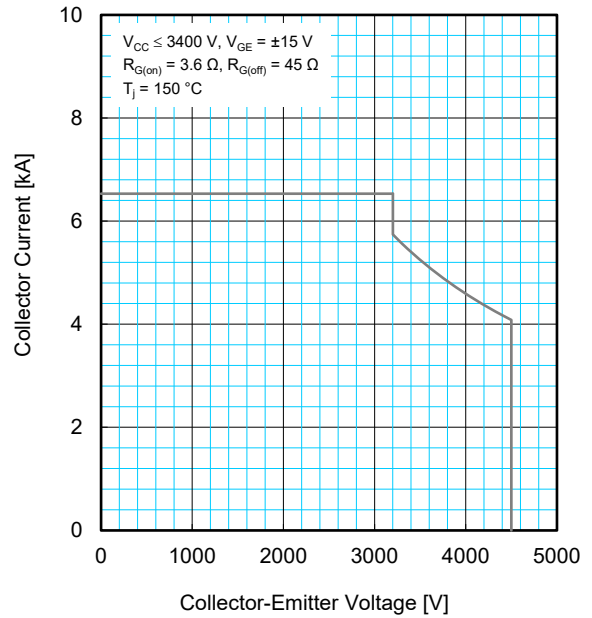
5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## PERFORMANCE CURVES

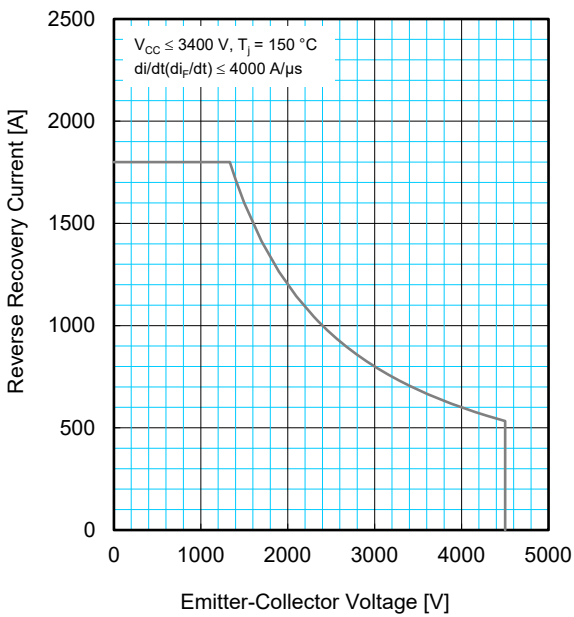
**REVERSE BIAS SAFE OPERATING AREA (RBSOA)**



**SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)**



**FREE-WHEEL DIODE / CLAMP DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)**



## CM900E2G-90X

HIGH POWER SWITCHING USE

INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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## CM900E2G-90X

HIGH POWER SWITCHING USE

INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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