TECHNICAL BULLETIN

[Issue No.] T09-0014   **[Page]** 1/2
[Title] Product discontinuation of QnA series   **[Date of Issue]** May, '04
        serial communication modules
[Relevant Models] AJ71QC24, AJ71QC24-R2, AJ71QC24-R4, A1SJ71QC24,
                     A1SJ71QC24-R2

Thank you for your continued support of Mitsubishi programmable logic controllers, MELSEC-QnA series.

Production of the following MELSEC-QnA series models will be discontinued.

1. Models to be discontinued

Product name	Model
QnA series serial communication module	AJ71QC24
	AJ71QC24-R2
	AJ71QC24-R4
	A1SJ71QC24
	A1SJ71QC24-R2

2. Schedule

• Order acceptance   : Through November, 2004
• Production discontinuation: Through December, 2004

3. Reasons for discontinuing production

(1) Some parts of the above products are now obsolete. Therefore, we will have difficulty to maintain the production system.

(2) Compatible models (AJ71QC24N, AJ71QC24N-R2, AJ71QC24N-R4, A1SJ71QC24N, A1SJ71QC24N-R2) have been released. (April, 1998)

4. Repair acceptance

• Repair acceptance   : Through December, 2011 (For 7 years after production discontinuation)

5. Alternative models

When replacing the discontinued model with the alternative, there is no need to modify the programs and parameters. However, pay attention to the precautions described on the next page.

Discontinued model	Alternative model
AJ71QC24	AJ71QC24N
AJ71QC24-R2	AJ71QC24N-R2
AJ71QC24-R4	AJ71QC24N-R4
A1SJ71QC24	A1SJ71QC24N
A1SJ71QC24-R2	A1SJ71QC24N-R2

TECHNICAL BULLETIN

(1) Precautions for program reuse

Because the internal processing speed of the alternative model has been improved, the time required for accessing to the PLC CPU and transmitting messages to the external device is shorter compared with the discontinued model. (Although the functions usage is the same, the performance and response speed are changed.)

When reusing the existing program, always confirm the operation and adjust the communication timing if necessary. Examples of the communication timing adjustment are shown below.

(a) When communicating by dedicated protocol

Specify the "Message wait" time to adjust the timing in the following cases.

Particularly, adjustment is necessary when "Scanning time of the QnACPU on the alternative model installed station > Message wait time".

1) When communication is performed with the QnA frame or QnA extension frame, specify in the alternative model buffer memory (at address 11E$_H$/1BE$_H$).

2) When communication is performed with the A-compatible frame, specify it in the request message transmitted from the external device.

(b) When communicating by nonprocedural or bidirectional protocol

Delay the data transmission timing.

• Data transmission from external device to alternative model

Delay the data transmission timing from the external device. (Adjust on the external device end.)

• Data transmission from alternative model to external device

In the case of the nonprocedural protocol, modify the sequence program to delay the data transmission from the alternative model to the external device by one scan.

(When the bidirectional protocol is used, delay the turn-on timing of the reception data read complete signal by one scan.)

(2) Precautions for internal current consumption (5V DC)

The alternative model has a slightly increased 5V DC internal current consumption value compared with the corresponding discontinued model.

Care should be taken so that it will not exceed the capacity of the power supply module used.

Discontinued model	Internal current consumption (5V DC)	Alternative model	Internal current consumption (5V DC)
AJ71QC24	0.3A	AJ71QC24N	0.4A
AJ71QC24-R2	0.2A	AJ71QC24N-R2	0.3A
AJ71QC24-R4	0.38A	AJ71QC24N-R4	0.6A
A1SJ71QC24	0.24A	A1SJ71QC24N	0.35A
A1SJ71QC24-R2	0.155A	A1SJ71QC24N-R2	0.3A