

TECHNICAL BULLETIN

[Issue No.] FA-A-0059

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[Title] Product discontinuation of the A1SD51S intelligent communication module

[Date of Issue] July 2009

[Relevant Models] A1SD51S

Thank you for your continued support of Mitsubishi programmable controllers, MELSEC series.

Production of the following MELSEC-AnS series model will be discontinued.

1. Model to be discontinued

Product name	Model
A1SD51S Intelligent communication module	A1SD51S

2. Schedule

- Transition to made-to-order production: December 31, 2009
- Order acceptance: Through May 31, 2010
- Production discontinuation: June 30, 2010

3. Reasons for discontinuing production

Some parts of the above product are now obsolete. Therefore, we will have difficulty to maintain the production system.

4. Repair acceptance

- Repair acceptance: Through June 30, 2017 (For 7 years after production discontinuation)

5. Alternative models

Discontinued model	Alternative model ^{*1}
A1SD51S	<ul style="list-style-type: none">• QD51 (RS-232: 2 channels)• QD51-R24 (RS-232: 1 channel, RS-422/485: 1 channel)

To replace the discontinued model with the QD51 or QD51-R24, transition to a MELSEC-Q series system is required. Note that MELSEC-AnS series modules can be utilized on MELSEC-Q series systems by using extension base units QA1S6□B.

For use of the QA1S6□B, applicable CPU and other module models are restricted.

For details, refer to the QCPU User's Manual (Hardware Design, Maintenance and Inspection).

*1 In some applications, a computer link module (A1SJ71UC24-R2/R4/PRF) or a serial communication module (QJ71C24N(-R2/R4)) can be used as an alternative.

For details, refer to "9. No-protocol mode of a computer link module (A1SJ71UC24-R2/R4/PRF)" or "10. Pre-defined protocol function of the serial communication module (QJ71C24N(-R2/R4)).

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6. Differences in performance specifications between the A1SD51S and the QD51(-R24)

The following table shows differences in performance specifications.

Item		A1SD51S	QD51	QD51-R24
General input/output		Input: 27 points Output: 23 points (2 points: For task start from the programmable controller CPU)	Input: 26 points Output: 23 points (2 points: For task start from the programmable controller CPU)	
Interface	RS-232	2 channels (D-sub 9-pin)	2 channels (D-sub 9-pin)	1 channel (D-sub 9-pin)
	RS-422/485	1 channel (D-sub 25-pin)	-	1 channel (Two-piece terminal block)
Power failure protection		Yes	None Refer to "8. (3) (c) Common memory reading and writing".	
Console		A7PHP, A7HGP, A7LMS, VG-620, VT-382, PC/AT personal computer	PC/AT personal computer	

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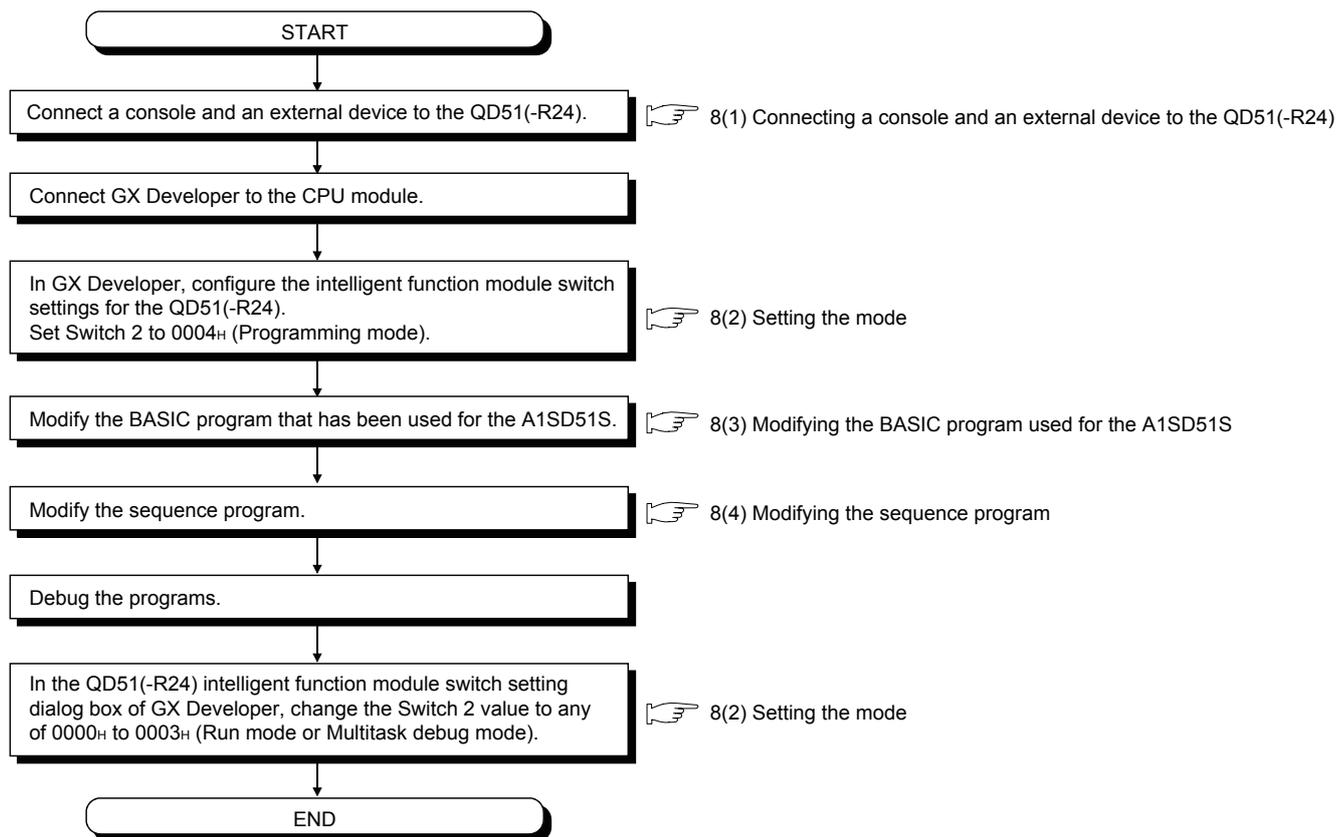
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7. Replacement procedure

The following is the procedure for replacing the A1SD51S with the QD51(-R24).



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8. Replacing the A1SD51S with the QD51(-R24)

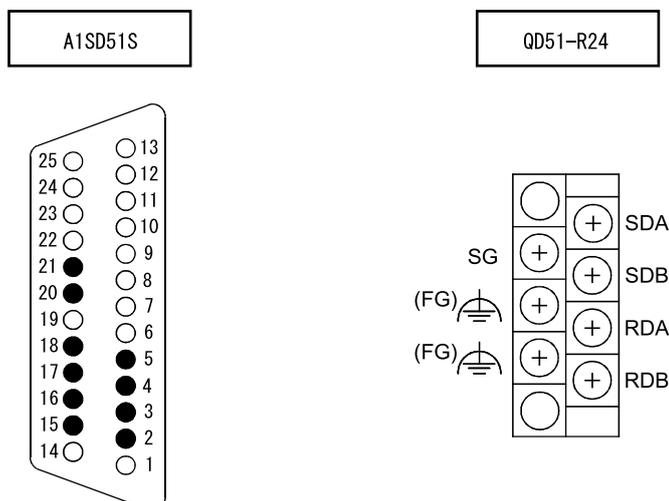
(1) Connecting a console and an external device to the QD51(-R24)

(a) Connection to the RS-232 interface

Since the same RS-232 interface specifications are applied to the QD51(-R24) and the A1SD51S, the existing cable and RS-232 converter can be reused.

(b) Connection to the RS-422/485 interface

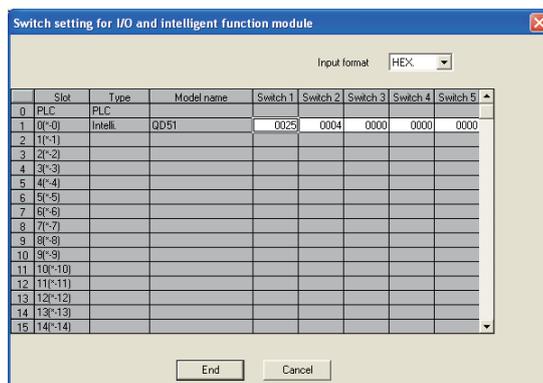
Because the QD51(-R24) uses a two-piece terminal block, the existing connector for the A1SD51S cannot be reused. In addition, wiring must be checked and changed since the signals are assigned to different positions.



For connection details, refer to the Q Corresponding Intelligent Communication Module User's Manual, "RS-232 Interface Specifications".

(2) Setting the mode

The mode of the QD51(-R24) is set in the intelligent function module setting dialog box of GX Developer while the mode of the A1SD51S is set with the mode setting switch on the module.



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(a) Switch 1

Bit position	Contents	OFF(0)	ON(1)
b0	Console	Channel setting	
b1		Refer to *1	
b2	Software	Not used	Used
b3	Not used	Fixed on OFF	
b4	Console, debugger type	VT	VG
b5	Ctrl + C setting	Disabled	Enabled
b6	Not used	Fixed on OFF	
b7	Access possible time setting after Y reset	200 ms	2000 ms

*1 Console (Channel setting)

Bit position		Contents
b1	b0	
OFF	OFF	Without console
OFF	ON	With console CH.1
ON	OFF	With console CH.2
ON	ON	Setting impossible

Bit position	Contents	OFF(0)	ON(1)
b8	Not used	Fixed on OFF	
b9	Flash ROM write protection setting	Without	With
bA	Not used	Fixed on OFF	
bB		Fixed on OFF	
bC	Debugger	Channel setting	
bD		Refer to *2	
bE	Software	Not used	Used
bF	Not used	Fixed on OFF	

*2 Debugger (Channel setting)

Bit position		Contents
bD	bC	
OFF	OFF	Without a debugger
OFF	ON	With debugger CH.1
ON	OFF	With debugger CH.2
ON	ON	Setting impossible

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(b) Switch 2

Set value	Description	Remark
0000H	Run mode	Mode for executing the BASIC program.
0001H		
0002H	Multitask debug mode	Mode for debugging by connecting a debugger.
0003H		
0004H	Programming mode	Mode for programming by connecting a console.
0005H to 000FH	Setting prohibited	-

(c) Switch 3 to Switch 5

Set 0000H.

(3) Modifying the BASIC program used for the A1SD51S

Programs used for the A1SD51S can be utilized for the QD51(-R24).

However, modification of the BASIC program may be required since the following setting items may be different between these models.

(a) Channels

Available channels differ for each model of the QD51(-R24).

If a channel not available for the QD51(-R24) has been used on the A1SD51S, modify the BASIC program and change the channel setting.

Item	A1SD51S	QD51	QD51-R24
CH.1 (RS-232)	○	○	○
CH.2 (RS-232)	○	○	×
CH.3 (RS-422/485)	○	×	○

○ : Available, × : N/A

(b) Data bit and stop bit

The data bit and stop bit settings differ.

Correct the BASIC program.

Item	A1SD51S	QD51	QD51-R24
Data bit	5, 6, 7, 8	7, 8	7, 8
Stop bit	1, 1.5, 2	1, 2	1, 2

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(c) Common memory reading and writing

The common memory of the QD51(-R24) is not backed up with a battery.

If a backup is needed, use the file register (R) in a programmable controller CPU.

In that case, change the part for accessing the common memory with the GETMEM and PUTMEM commands to the part for accessing the file register (R) with the PCRD and PCWT commands.

(d) PCRD and PCWT commands

The following processing codes for the A1SD51S cannot be executed for QCPUs and QnACPU.

For processing available for the alternative, change the processing code.

(PCRD command)

Processing codes for the A1SD51S		Processing codes for the QD51(-R24)	
Processing Code	Description	Processing Code	Description
2(&H2)	Monitoring of the device monitor register by the PCWT command	515(&H203)	Reading of Q/QnA series device memory
4(&H4)	Reading of expansion file register data		
5(&H5)	Monitoring the expansion file register monitor registered by the PCWT command		
7(&H7)	Reading of continuous address designation data in the expansion file register		
8(&H8)	Reading of sequence programs	-	Not available for the Q/QnA series.
9(&H9)	Reading of microcomputer programs		
10(&HA)	Reading of comment data		
11(&HB)	Reading of expansion comment data		
12(&HC)	Reading of the special function module buffer memory	533(&H215)	Reading of Q/QnA series intelligent function module buffer memory
13(&HD)	Reading of programmable controller CPU model name	513(&H201)	Reading of Q/QnA series programmable controller CPU model name
14(&HE)	Reading of parameter data(Reading of MELSECNET/10 parameters)	-	Not available for the Q/QnA series.
21(&H15)	Reading of network information		
22(&H16)	Reading of routing parameters		

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(PCWT command)

Processing codes for the A1SD51S		Processing codes for the QD51(-R24)	
Processing Code	Description	Processing Code	Description
2(&H2)	Device memory monitor registration	515(&H203)	Writing to Q/QnA series device memory
3(&H3)	Random writing to device memory		
4(&H4)	Writing to expansion file register		
5(&H5)	Expansion file register monitor registration		
6(&H6)	Random writing to expansion file register		
7(&H7)	Continuous address designation data writing to expansion file register		
8(&H8)	Writing of sequence program	-	Not available for the Q/QnA series.
9(&H9)	Writing of microcomputer program		
10(&HA)	Writing of comment data		
11(&HB)	Writing of expansion comment data		
12(&HC)	Writing to special function module buffer memory	533(&H215)	Writing to Q/QnA series intelligent function module buffer memory
14(&HE)	Writing of parameter data (Writing of MELSECNET/10 parameters)	-	Not available for the Q/QnA series.
15(&HF)	Analysis of parameter data		

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(e) DATE\$ function

Modify the BASIC program since the DATE\$ function processing is different between the discontinued and alternative models.

Processing	A1SD51S	QD51(-R24)	Action
Reading	Reads only the last two digits of the year. If the last two digits are 90 or more, the first two digits are 19. If the last two digits are less than 90, the first two digits are 20.	Reads the four-digit year as it is.	Modify the program to use four-digit year processing.
Writing	Writes only the last two digits of the year.	Writes the four-digit year as it is.	Designate the year as four digits correctly.

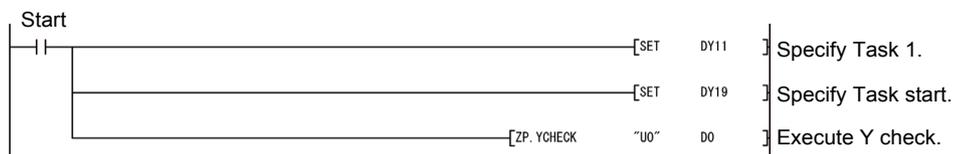
(4) Modifying the sequence program

When the Task start signal (Y19) has been used to start a task, add the YCHECK instruction in the sequence program.

On the A1SD51S, an interrupt occurs upon turn-ON of the Task start signal (Y19), which starts a task.

The QD51(-R24) monitors the Task start signal (Y19) at intervals of 10ms for starting a task.

By executing the YCHECK command as shown below, the task can be started at the same timing as the A1SD51S.



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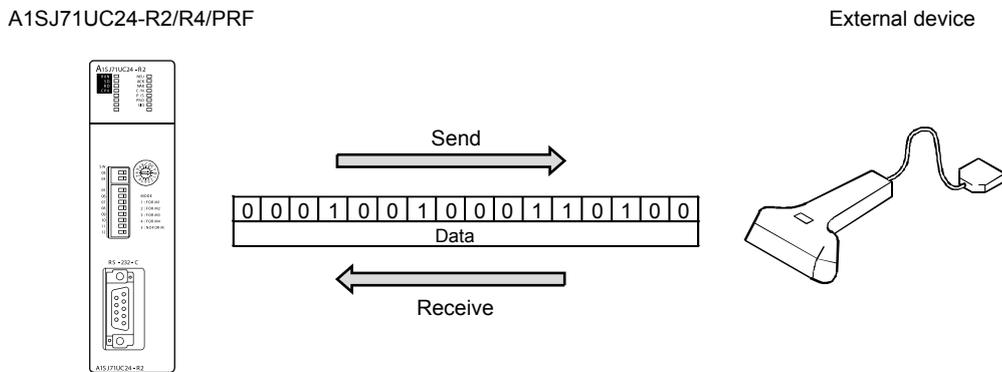
[Relevant Models] A1SD51S

9. No-protocol mode of a computer link module (A1SJ71UC24-R2/R4/PRF)

When the A1SD51S has been used only for data communication with external devices and you want to utilize the existing MELSEC-AnS series system without change, a computer link module (A1SJ71UC24-R2/R4/PRF) can be used instead of the A1SD51S.

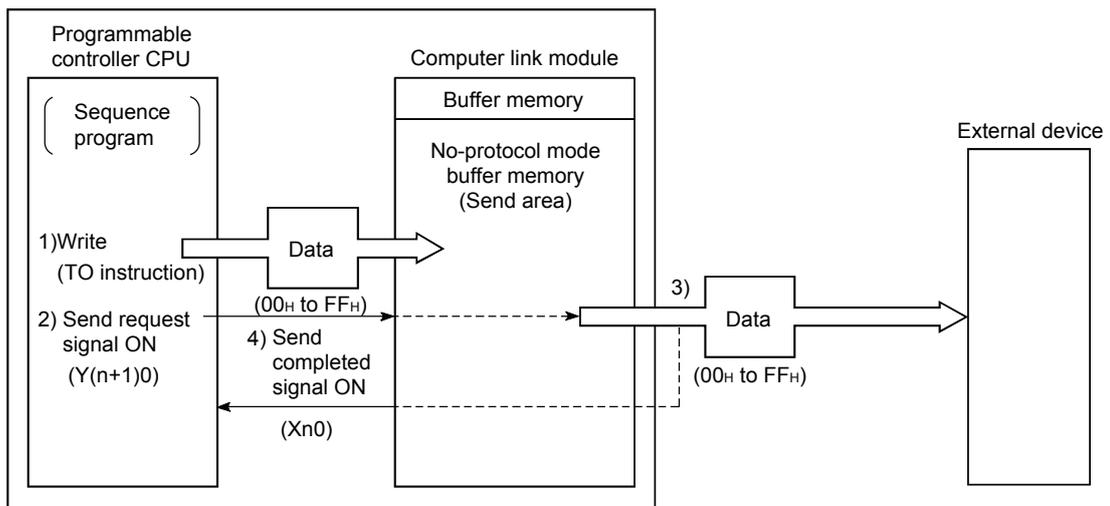
However, the BASIC program for some processing (e.g. conversion between strings and numerals (ASCII and BIN), data sorting, etc.) needs to be replaced with a sequence program.

In the no-protocol mode, a user-defined message format is used for communication with an external device.



(1) When the A1SJ71UC24-R2/R4/PRF sends data to an external device

The data to be sent are stored in the buffer memory of the A1SJ71UC24-R2/R4/PRF, and the Send request signal (Y(n+1)0) is turned on.



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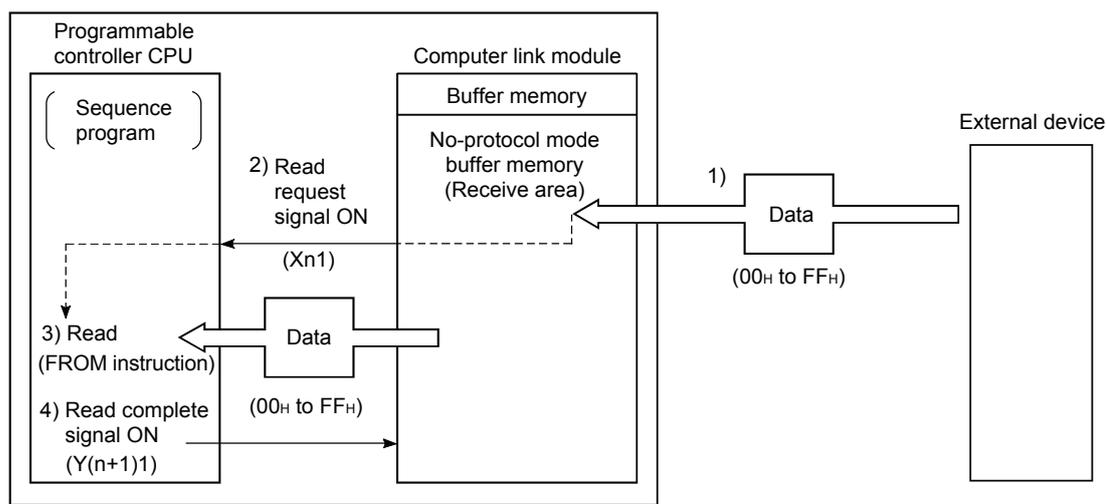
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(2) When the A1SJ71UC24-R2/R4/PRF receives data from an external device

The Read request signal (Xn1) is turned on, and the received data are read out from the buffer memory of the A1SJ71UC24-R2/R4/PRF.



10. Pre-defined protocol function of the serial communication module (QJ71C24N(-R2/R4))

If the A1SD51S has been used for data communication with an external device, the pre-defined protocol function of the QJ71C24N(-R2/R4) can be utilized for it.

The following operations, which have been performed from a BASIC program of the A1SD51S, can be registered to the flash ROM of the QJ71C24N(-R2/R4) after setting protocols on the GX Configurator-SC screen.

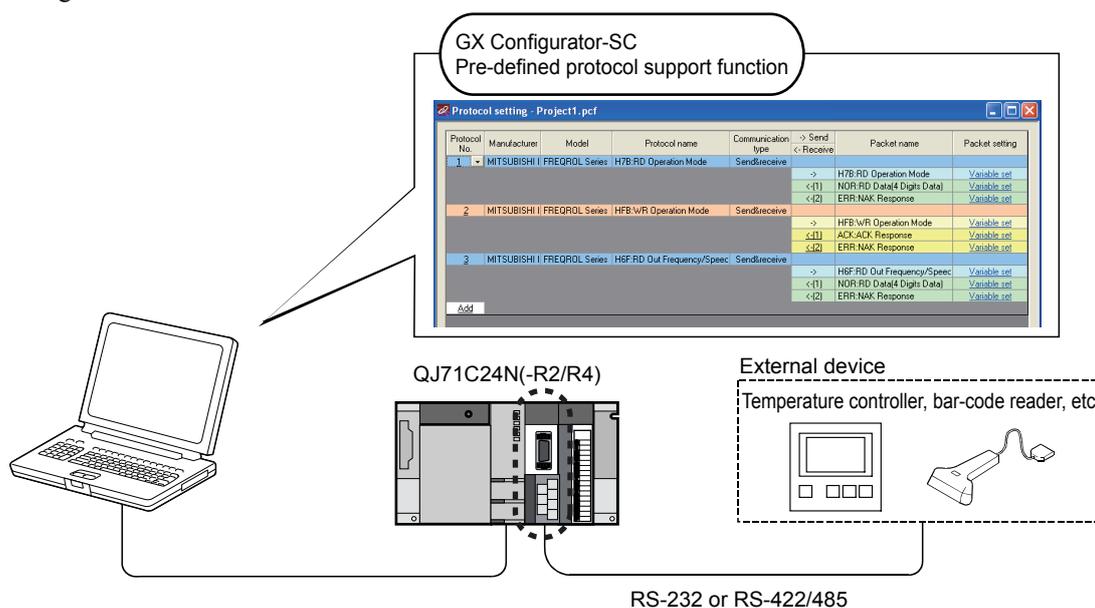
- Communication setting for RS-232 and RS-422/485
- Communication data conversion between strings and numbers (ASCII-BIN conversion)
- Processing such as writing received data to CPU devices

(1) Features of the pre-defined protocol function of the QJ71C24N(-R2/R4)

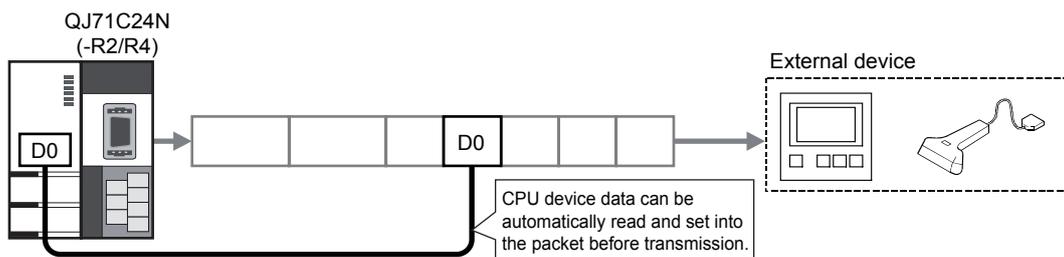
(a) Protocols for communication with an external device can be created easily on the GX Configurator-SC setting screen.

Without creating sequence programs, communication protocols (including a communication procedure) can be created and registered.

Also, communication data conversion between strings and numbers (ASCII-BIN conversion) can be set on the GX Configurator-SC screen.



(b) Data to be sent can be read from CPU devices to the QJ71C24N(-R2/R4), and data received by the QJ71C24N(-R2/R4) can be written to CPU devices.



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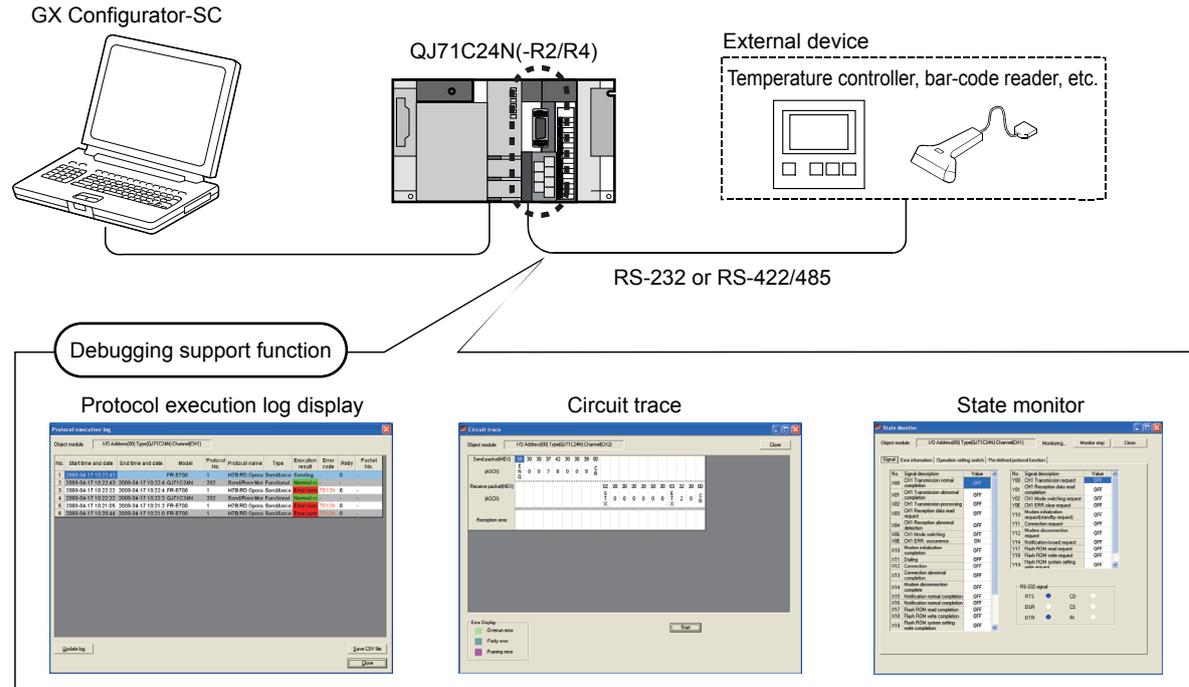
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(c) The debugging support function of GX Configurator-SC allows line data and communication signal checking and state monitoring, making debugging easy even without a line analyzer.



(e) Data configured in GX Configurator-SC can be registered to the flash ROM of the QJ71C24N(-R2/R4).

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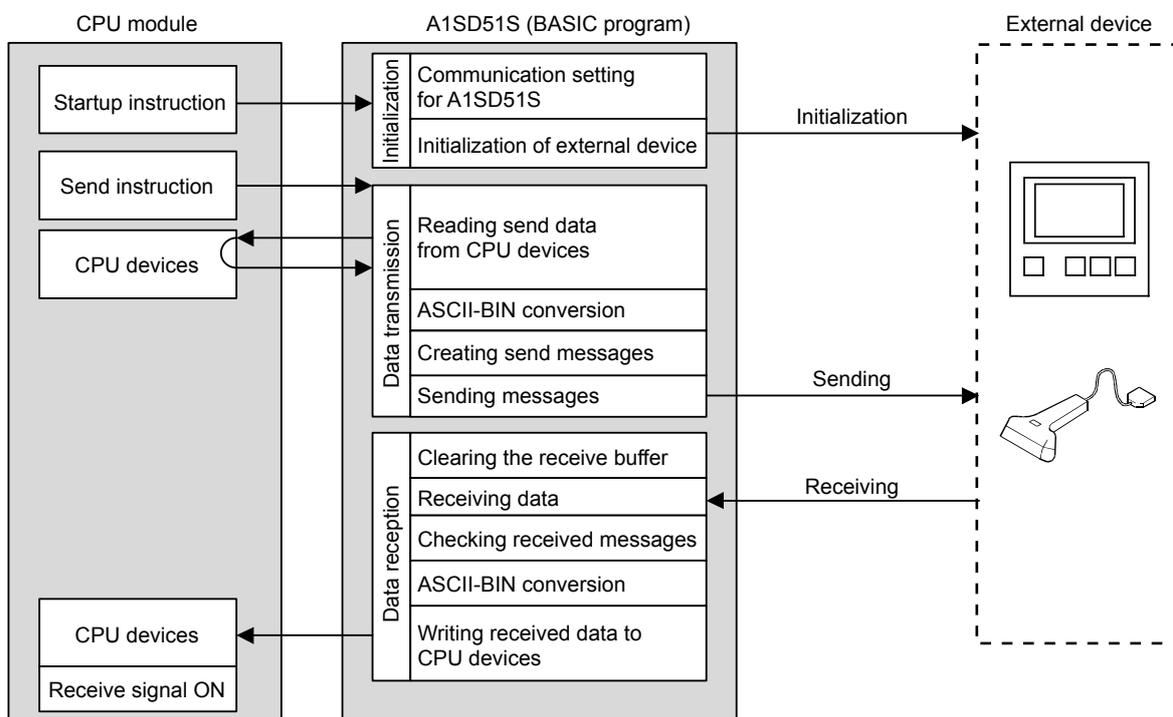
[Relevant Models] A1SD51S

(2) Operation overview

Communication processing with an external device differs between the A1SD51S and the pre-defined protocol function of the QJ71C24N(-R2/R4) as described below.

(a) When the A1SD51S is used

The BASIC program of the A1SD51S initializes the external device, sends data, and receives data.



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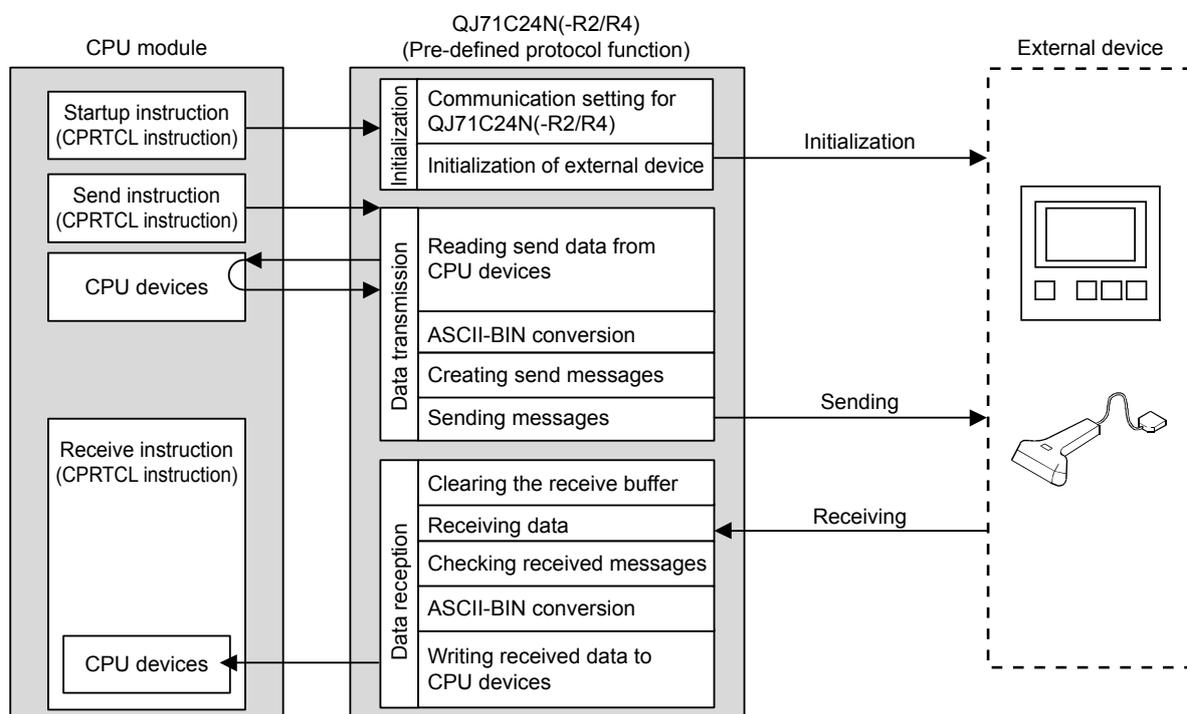
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(b) When the pre-defined protocol function of the QJ71C24N(-R2/R4) is used

In GX Configurator-SC, an initialization packet for communication with the external device and send and receive packets can be set, and these settings can be registered to the flash ROM of the QJ71C24N(-R2/R4).

Once a pre-defined protocol is registered, data can be sent or received by specifying a packet No. in the sequence program (CPRTCL instruction).

Up to 256 packets can be registered to the flash ROM of the QJ71C24N(-R2/R4).



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For the initialization, send, and receive packets of the pre-defined protocol function, conversion data options can be selected in combination so that the setting will be suitable for the external device.

Header	Data 1	Data 2	• • •	Data n	Terminator
--------	--------	--------	-------	--------	------------

Element	Setting	Example
Header	Any ASCII string or binary code can be set.	"STX", "ENQ", 1AB2C3
Data	The data part can be set. The data part settings can be altered as follows: <ul style="list-style-type: none"> • HEX→ASCII or ASCII→HEX (decimal or hexadecimal) • Conversion size: word or double word • Byte swap: enable or disable • Number of data: fixed or variable • Blank padding: 0 or space • Sign character: none, +, 0, or space • Delimiter: none, comma, or space 	-
Terminator	Any ASCII string or binary code can be set. The following check codes are available. <ul style="list-style-type: none"> • Horizontal parity • Sum check • 16-bit CRC (MODBUS(R) specifications) • ASCII (decimal or hexadecimal) • Binary code • Complement calculation: enable or disable • Byte swap: enable or disable 	"ETX", [CR], 1AB2C3

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(3) A BASIC program example of the A1SD51S and the corresponding pre-defined protocol setting example for the QJ71C24N(-R2/R4)

The following BASIC program for bar-code reader connection to RS-232 of the A1SD51S can be replaced with the pre-defined protocol setting of the QJ71C24N(-R2/R4) as described later.

(a) BASIC program example of the A1SD51S (a bar-code reader is connected as an external device.)

```
10 /*-- Open communication port --*/
20 DIM RSSET1%(3), RSSET2%(4), RSSND%(4), RSRCV%(4), RSRCV2%(4), RDAT%(20)
30 RSSET1%(0) = 9600 : RSSET1%(1) = &H107 : RSSET1%(2) = &H1
40 RSSET2%(0) = 18 : RSSET2%(1) = &HF00 : RSSET2%(3) = 1024
50 RSSND%(0) = 1 : RSSND%(2) = 30
60 ZOPEN #CHN,RSSET1%()
70 ZCNTL #CHN, 0, RSSET2%()
80 /*-- Waiting for CPU instruction --*/
90 ON ERROR GOTO *GETERRPRC
100 WHILE(1)
110 IF ( B@(Y,INITIN) = 1 ) THEN GOSUB *INISYAL
120 IF ( B@(Y,READIN) = 1 ) THEN GOSUB *READSEQ
130 WEND
140 END
150 '
160 *READSEQ
170 RCVNG = 0
180 RSRCV2%(0) = 80
190 RSRCV2%(2) = 0
200 ZCNTL #CHN,0,RSRCV2%()
210 RCVDAT$ = SPACES(15)+CHRS(0)+CHRS(0)
220 SNDBUF$ = "G" : GOSUB *TXTSEND : GOSUB *TXTRCV
230 IF ( LEN(RCVDAT$) <> BARLEN ) AND ( BARLEN > 0 ) THEN RCVOK = 0
240 RCVDAT$ = LEFT$(( RCVDAT$ + SPACES(15) ), 15 ) '
250 PUTMEM TO BARBUF, &H20 FROM RCVDAT$, 0, 17
260 IF( RCVOK = 0 ) THEN B@(X,READNG) = 1
270 IF( RCVOK = 1 ) THEN B@(X,READOK) = 1
280 WHILE ( B@(Y,READIN) = 1 ): WEND
290 B@(X,READOK) = 0 : B@(X,READNG) = 0
300 RETURN
310 '
320 *TXTRCV
330 RCVBUF$ = ""
340 RCVOK = 0
```

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```
350 RCVBYT$ = SPACE$(1)
360 FTIMEOUT = 0
370 RSRCV%(0) = 1
380 RSRCV%(1) = 0
390 RSRCV%(2) = 30
400 ZRECEIVE #CHN, 0, RSRCV%(), RCVBYT$
410 IF ( FTIMEOUT = 0 ) THEN RCVBUF$ = RCVBYT$
420 ZCNTL #CHN,0,RSRCV2%() 'Get number of received data
430 WHILE( (FTIMEOUT = 0) AND ((0 <> RSRCV2%(2)) OR (RCVOK = 0)) )
440 RCVBYT$ = SPACE$(1)
450 RSRCV%(0) = 1
460 RSRCV%(1) = 0
470 RSRCV%(2) = 0
480 ZRECEIVE #CHN, 0, RSRCV%(), RCVBYT$
490 IF ( RCVOK = 0 ) THEN RCVBUF$ = RCVBUF$ + RCVBYT$
500 IF ( RCVBYT$ = CHR$(3) ) THEN RCVOK = 1
510 ZCNTL #CHN,0,RSRCV2%() 'Get number of received data
520 WEND
530 RETURN
540 '
550 *INISYAL
560 RDAT% = 0 'Bar-code reader initialization
570 GETMEM TO RDAT%(0),0 FROM BARBUF,0,18
580 B@(X,INITOK) = 0
590 B@(X,INITNG) = 0
600 '
610 SNDBUF$ = "SSET": GOSUB *INISEND
620 SNDBUF$ = "CODE3=5": GOSUB *INISEND
630 SNDBUF$ = "WP12"+CHR$(&H30+MULTILBL): GOSUB *INISEND
640 SNDBUF$ = "SAVE": GOSUB *INISEND
650 SNDBUF$ = "SEND": GOSUB *INISEND
660 IF( B@(X,INITNG) = 0 ) THEN B@(X,INITOK) = 1
670 WHILE ( B@(Y,INITIN) = 1 ): WEND
680 B@(X,INITOK) = 0
690 B@(X,INITNG) = 0
700 RETURN
710 '
```

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```
720 *INISEND
730 IF( B@(X,INITNG) <> 0 ) THEN RETURN
740 GOSUB *TXTSEND
750 RCVBUF$ = SPACE$(3)
760 RSRCV%(0) = 3
770 RSRCV%(1) = 0
780 RSRCV%(2) = 30
790 FTIMEOUT = 0
800 ZRECEIVE #CHN, 0, RSRCV%(), RCVBUF$
810 IF(( RCVBUF$ <> "OK"+CHR$(13) ) OR ( FTIMEOUT = 1 )) THEN B@(X,INITNG) = 1
820 RETURN
830 '
840 *TXTSEND
850 SNDBUF$ = SNDBUF$ + CHR$(13)
860 RSSND%(0) = LEN( SNDBUF$ )
870 RSSND%(2) = 30
880 FTIMEOUT = 0
890 ZSEND #CHN,0,RSSND%(),SNDBUF$
900 RETURN
910 /*-- END --*/
```

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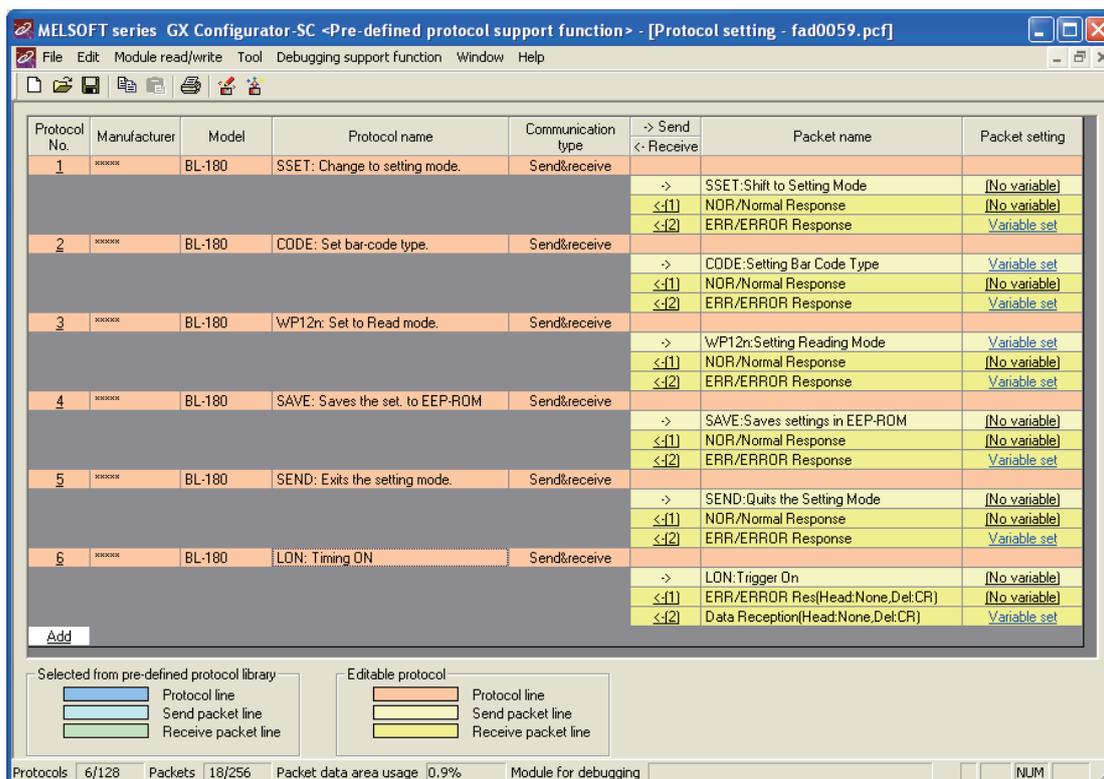
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(b) Pre-defined protocol setting example for the QJ71C24N(-R2/R4)

Registering the following pre-defined protocols to the flash ROM of the QJ71C24N(-R2/R4) and specifying a packet No. in the sequence program (CPRTCL instruction) will allow communication with the external device.

1) GX Configurator-SC (Pre-defined protocol support function) setting



Configure protocol No.2 and No.3 so that the data to be sent will be read from the following CPU devices and set into a send packet.

Item	Sending data storage area
Protocol No.2	D100 (Code Number) D101 (Bar Code Type)
Protocol No.3	D102 (Parameter)

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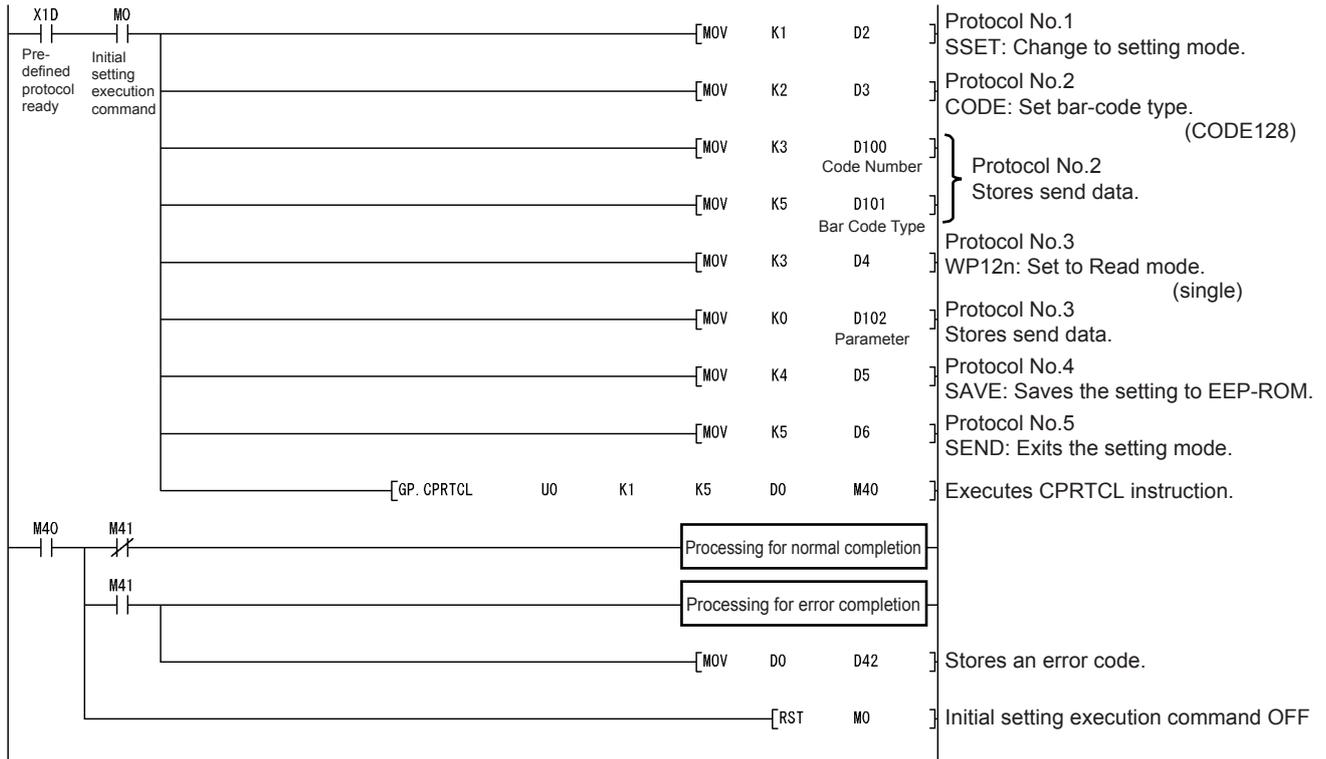
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[Relevant Models] A1SD51S

2) Program example

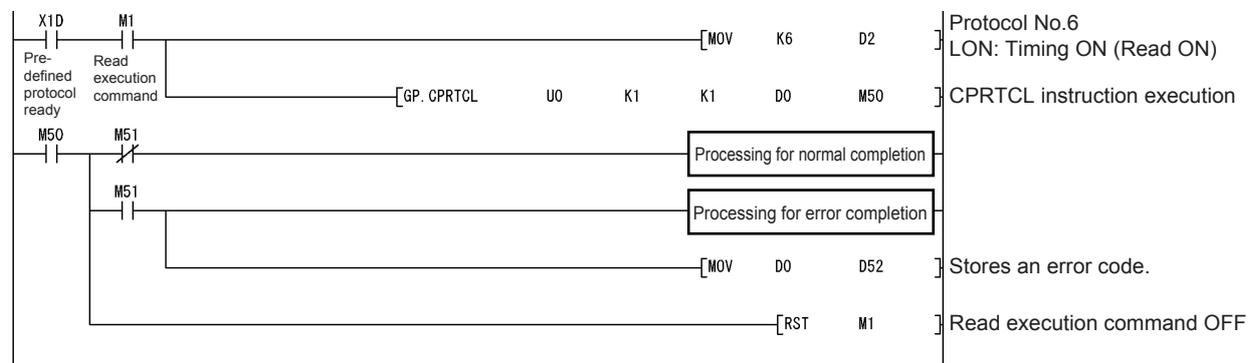
- Initialization of a bar-code reader

Single mode, initial setting



- Instruction for reading the bar-code reader

Single mode execution



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11. Related manuals

For replacement, refer to the following manuals.

(1) Details of the QD51(-R24)

Manual name	Manual number (model code)
Q Corresponding Intelligent Communication Module User's Manual	SH-080089 (13JR16)

(2) No-protocol mode details of the A1SJ71UC24-R2/R4/PRF

Manual name	Manual number (model code)
Computer Link Module (Com. link func./Print. func.) User's Manual	SH-3511 (13JE77)

(3) Details of the pre-defined protocol function of the QJ71C24N(-R2/R4)

Manual name	Manual number (model code)
GX Configurator-SC Version 2 Operating Manual (Pre-defined protocol support function)	SH-080850ENG (13JU66)