TECHNICAL BULLETIN

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[Title] Measures Regarding Year 2000 Problem for the Intelligent Communication Module [Date of Issue] July '99

[Relevant Models] AD51H, AD51H-S3 and A1SD51S

Thank you for your continued patronage of the Mitsubishi general-purpose PLC MELSEC-A Series.

The year 2000 problem includes recognizing the years before 1999 and after 2000, and compensating the date in leap years.
This will be handled as follows for the AD51H, AD51H-S3 and A1SD51S type intelligent communication modules.
The A1SD51S has been added as a relevant model in this revision Version A.
The explanations have not been added or revised.

The AD51H, AD51H-S3 and A1SD51S do not have a built-in clock element, so the clock data is read from the CPU module and used.

The clock data of the last two digits of the year read from the CPU module is automatically converted (effective year range: 1990 to 2089) into a four-digit year inside this module. Thus, the year data does not need to be compensated with the user program.

Whereas

1. Compensation of date in leap years

   The clock element built into the CPU module will automatically compensate the date, so the user does not need to reset the date in the clock element.

2. Recognition of year following year 2000

   Only the last two digits of the year are read from the CPU module's clock element. However, this is automatically converted inside this module into a four-digit year data having an effective year range of 1990 to 2089. The "DATE$" function that returns the year data as the return value also returns a four-digit value, so the year data does not need to be compensated with the user program.